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| Titolo                  | Formal Techniques for Distributed Objects, Components, and Systems : 36th IFIP WG 6.1 International Conference, FORTE 2016, Held as Part of the 11th International Federated Conference on Distributed Computing Techniques, DisCoTec 2016, Heraklion, Crete, Greece, June 6-9, 2016, Proceedings / / edited by Elvira Albert, Ivan Lanese   |
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| Edizione                | [1st ed. 2016.]  |
| Descrizione fisica      | 1 online resource (XVI, 275 p. 56 illus.)  |
| Collana                 | Programming and Software Engineering, , 2945-9168 ; ; 9688   |
| Disciplina              | 004.0151   |
| Soggetti                | Computer science<br>Software engineering<br>Machine theory<br>Application software<br>Computer networks<br>Computer Science Logic and Foundations of Programming<br>Software Engineering<br>Theory of Computation<br>Formal Languages and Automata Theory<br>Computer and Information Systems Applications<br>Computer Communication Networks  |
| Lingua di pubblicazione | Inglese  |
| Formato                 | Materiale a stampa   |
| Livello bibliografico   | Monografia   |
| Note generali           | Includes index.  |
| Nota di contenuto       | On the Power of Attribute-Based Communication -- Fencing Programs with Self-Invalidation and Self-Downgrade -- A Framework for Certified Self-Stabilization -- Developing Honest Java Programs with Diogenes -- Playing with our CAT and Communication-Centric Applications -- Multiparty Session Types Within A Canonical Binary Theory, and Beyond -- A Type Theory for Robust Failure Handling in Distributed Systems -- Choreographies in Practice -- Specification-Based Synthesis of Distributed Self-Stabilizing Protocols -- Branching Bisimulation Games -- A Configurable CEGAR Framework with Interpolation-Based |

Refinements -- A Theory for the Composition of Concurrent Processes  
-- Enforcing Availability in Failure-Aware Communicating Systems --  
Ransomware Steals Your Phone. Formal Methods Rescue It -- Multiple  
Mutation Testing from FSM -- The Challenge of Typed Expressiveness  
in Concurrency -- Type-Based Analysis for Session Inference --  
SimAutoGen Tool: Test Vector Generation from Large Scale  
Matlab/Simulink Models.

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Sommario/riassunto

This book constitutes the proceedings of the 36th IFIP WG 6.1  
International Conference on Formal Techniques for Distributed Objects,  
Components, and Systems, FORTE 2016, held in Heraklion, Crete,  
Greece, in June 2016, as part of the 11th International Federated  
Conference on Distributed Computing Techniques, DisCoTec 2016. The  
18 revised full papers presented were carefully reviewed and selected  
from 44 submissions. The papers present a wide range of topics on  
distributed computing models and formal specification, testing, and  
verification methods.

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