Record Nr. UNINA9910484015203321 Reversible Computation: 6th International Conference, RC 2014, Kyoto, **Titolo** Japan, July 10-11, 2014. Proceedings / / edited by Shigeru Yamashita. Shin-ichi Minato Cham:,: Springer International Publishing:,: Imprint: Springer,, Pubbl/distr/stampa **ISBN** 3-319-08494-1 Edizione [1st ed. 2014.] 1 online resource (X, 231 p. 105 illus.) Descrizione fisica Programming and Software Engineering;; 8507 Collana 004 Disciplina Soggetti Logic design Data structures (Computer science) Computers Algorithms Software engineering Computer science Logic Design Data Structures and Information Theory Computation by Abstract Devices Algorithm Analysis and Problem Complexity Software Engineering Computer Science, general Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Bibliographic Level Mode of Issuance: Monograph Note generali Nota di contenuto Automata for reversible computation -- Notation and languages for reversible computation -- Synthesis and optimization for reversible circuits -- Validation and representation of quantum logic. This book constitutes the refereed proceedings of the 6th International Sommario/riassunto Conference on Reversible Computation, RC 2014, held in Kyoto, Japan, in July 2014. The 14 contributions presented together with three invited talks were carefully reviewed and selected from 27 submissions. The papers are organized in topical sections on automata for reversible

computation; notation and languages for reversible computation;

synthesis and optimization for reversible circuits; validation and representation of quantum logic.