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Soggetti	Software engineering Computer logic Programming languages (Electronic computers) Mathematical logic Computer communication systems Computer programming Software Engineering Logics and Meanings of Programs Programming Languages, Compilers, Interpreters Mathematical Logic and Formal Languages Computer Communication Networks Programming Techniques
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Nota di contenuto	Verifying MARTE/CCSL Mode Behaviors Using UPPAAL -- A Transformation Approach for Multiform Time Requirements -- Real-Time Migration Properties of rTiMoVerified in Uppaal -- Verified Protocol to Implement Multi-way Synchronisation and Interleaving in CSP -- From Extraction of Logical Specifications to Deduction-Based Formal Verification of Requirements Models -- Model Checking of Security-Critical Applications in a Model-Driven Approach -- Lifting Verification Results for Preemption Statements -- Rule-Level

Verification of Graph Transformations for Invariants Based on Edges' Transitive Closure -- Sound Symbolic Linking in the Presence of Preprocessing -- Inferring Physical Units in B Models -- A Tool for Behaviour-Based Discovery of Approximately Matching Web Services -- A Type System for Components -- Early Fault Detection in DSLs Using SMT Solving and Automated Debugging -- Static Detection of Implementation Errors Using Formal Code Specification -- Compositional Static Analysis for Implicit Join Synchronization in a Transactional Setting -- {log} as a Test Case Generator for the Test Template Framework -- Zero Overhead Runtime Monitoring -- Run-Time Verification of Coboxes -- Automated Mediator Synthesis: Combining Behavioural and Ontological Reasoning -- Program Transformation Based on Symbolic Execution and Deduction -- Constraint Specification and Test Generation for OSEK/VDX-Based Operating Systems.

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Sommario/riassunto

This book constitutes the refereed proceedings of the 11th International Conference on Software Engineering and Formal Methods, SEFM 2013, held in Madrid, Spain, in September 2013. The 21 full papers included in this volume were carefully reviewed and selected from 58 submissions. They are organized in topical sections on real-time systems, verification, types and inference, static analysis, testing and runtime verification, and synthesis and transformation.

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