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Software Infrastructures: Lessons Learned on the webinos Project -- SLAM: SLice And Merge - Effective Test Generation for Large Systems -- Improving Post-silicon Validation Efficiency by Using Pre-generated Data -- Development and Verification of Complex Hybrid Systems Using Synthesizable Monitors -- Assertion Checking Using Dynamic Inference -- Formal Specification of an Erase Block Management Layer for Flash Memory -- Attention-Based Coverage Metrics Synthesizing, Correcting and Improving Code, Using Model Checking-Based Genetic Programming -- Domain Types: Abstract-Domain Selection Based on Variable Usage -- Efficient Analysis of Reliability Architectures via Predicate Abstraction -- Lazy Symbolic Execution through Abstraction and Sub-space Search -- SPIN as a Linearizability Checker under Weak Memory Models -- Arithmetic Bit-Level Verification Using Network Flow Model -- Performance Evaluation of Process Partitioning Using Probabilistic Model Checking -- Improving Representative Computation in ExpliSAT.

Sommario/riassunto	This book constitutes the refereed proceedings of the 9th International Haifa Verification Conference, HVC 2013, held in Haifa, Israel in November 2013. The 24 revised full papers presented were carefully reviewed and selected from 49 submissions. The papers are organized in topical sections on SAT and SMT-based verification, software testing, supporting dynamic verification, specification and coverage, abstraction and model presentation.