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Soggetti	Microprocessors Programming languages (Electronic computers) Computer system failures Computer hardware Algorithms Processor Architectures Programming Languages, Compilers, Interpreters System Performance and Evaluation Computer Hardware Algorithm Analysis and Problem Complexity
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Note generali	Includes index.
Nota di contenuto	Tasking models and their optimization Understanding and verifying correctness of OpenMP programs OpenMP memory extensions Extensions for tools and locks Experiences with OpenMP device constructs.
Sommario/riassunto	This book constitutes the refereed proceedings of the 10th International Workshop on OpenMP, held in Salvador, Brazil, in September 2014. The 16 technical full papers presented were carefully reviewed and selected from 18 submissions. The papers are organized in topical sections on tasking models and their optimization; understanding and verifying correctness of OpenMP programs; OpenMP

memory extensions; extensions for tools and locks; experiences with OpenMP device constructs.