

1. Record Nr.	UNINA9910483563003321
Titolo	Computer performance evaluation and benchmarking : SPEC benchmark workshop 2009, Austin, TX, USA, January 25, 2009 ; proceedings // David Kaeli, Kai Sachs (eds.)
Pubbl/distr/stampa	Berlin ; ; Heidelberg ; ; New York, NY, : Springer, 2009
ISBN	3-540-93799-4
Edizione	[1st ed. 2009.]
Descrizione fisica	1 online resource (IX, 145 p.)
Collana	Lecture notes in computer science ; ; 5419
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Disciplina	004.0151
Soggetti	Computers - Power supply Benchmarking (Management)
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di contenuto	Benchmark Suites -- SPECrate2006: Alternatives Considered, Lessons Learned -- SPECjvm2008 Performance Characterization -- CPU Benchmarking -- Performance Characterization of Itanium® 2-Based Montecito Processor -- A Tale of Two Processors: Revisiting the RISC-CISC Debate -- Investigating Cache Parameters of x86 Family Processors -- Power/Thermal Benchmarking -- The Next Frontier for Power/Performance Benchmarking: Energy Efficiency of Storage Subsystems -- Thermal Design Space Exploration of 3D Die Stacked Multi-core Processors Using Geospatial-Based Predictive Models -- Modeling and Sampling Techniques -- Generation, Validation and Analysis of SPEC CPU2006 Simulation Points Based on Branch, Memory and TLB Characteristics -- A Note on the Effects of Service Time Distribution in the M/G/1 Queue.
Sommario/riassunto	This book constitutes the proceedings of the SPEC Benchmark Workshop 2009 held in Austin, Texas, USA on January 25th, 2009. The 9 papers presented were carefully selected and reviewed for inclusion in the book. The result is a collection of high-quality papers discussing current issues in the area of benchmarking research and technology. The topics covered are: benchmark suites, CPU benchmarking, power/thermal benchmarking, and modeling and sampling techniques.

