1. Record Nr. UNINA9910483174903321 Autore Jiang Dong Titolo Advanced pulse-width-modulation: with freedom to optimize power electronics converters / / Dong Jiang [and four others] Pubbl/distr/stampa Singapore:,: Springer,, [2021] ©2021 **ISBN** 981-334-385-0 Edizione [1st ed. 2021.] Descrizione fisica 1 online resource (XVII, 380 p. 453 illus., 332 illus. in color.) Collana **CPSS Power Electronics Series** Disciplina 621.317 Soggetti Pulse-duration modulation Power electronics Electronic circuits Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Nota di contenuto Fundamental of Power Electronics Converter and Pulse-Width-Modulation -- Principle of Pulse-Width-Modulation -- PWM's Impact on System Performance -- Current Ripple Prediction Model -- Variable Switching Frequency PWM -- Phase-shift PWM -- Advanced PWM for Advanced Topologies -- PWM for Common-mode Noise Reduction --PWM's Implementation in Hardware. This book is a technical publication for students, scholars and Sommario/riassunto engineers in electrical engineering, focusing on the pulse-widthmodulation (PWM) technologies in power electronics area. Based on an introduction of basic PWM principles this book analyzes three major challenges for PWM on system performance: power losses, voltage/current ripple and electromagnetic interference (EMI) noise, and the lack of utilization of control freedoms in conventional PWM technologies. Then, the model of PWM's impact on system performance is introduced, with the current ripple prediction method for voltage source converter as example. With the prediction model, two major advanced PWM methods are introduced: variable switching frequency

PWM and phase-shift PWM, which can reduce the power losses and EMI

for the system based on the prediction model. Furthermore, the advanced PWM can be applied in advanced topologies including

multilevel converters and paralleled converters. With more control variables in the advanced topologies, performance of PWM can be further improved. Also, for the special problem for common-mode noise, this book introduces modified PWM method for reduction. Especially, the paralleled inverters with advanced PWM can achieve good performance for the common-mode noise reduction. Finally, the implementation of PWM technologies in hardware is introduced in the last part.