1. Record Nr. UNINA9910482764103321

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| Autore | Anon |
| Titolo | Gaards Retthen, Huorledis skal holdis paa Kronens Slotte oc Gaarde, offuer alt Danmarckis oc Norgis Riiger, Sammeledis vdi Kiøbstederne, huor Kong: Maiest: eller hans Kong: Mtt's: Marskalck met hans Kong: Mtt's Folck oc Hoffsinder liggendis ere, Desligeste paa Adelens Gaarde eller andensteds, huor de selff eller deris Befalnings Mend oc Fogeder met deris Folck forsamlet er [[electronic resource]] |
| Pubbl/distr/stampa | Copenhagen, : Andreas Gutterwitz, 1577 |
| Descrizione fisica | Online resource ([39] bl.) |
| Lingua di pubblicazione | Danese |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |
| Note generali | Reproduction of original in Det Kongelige Bibliotek / The Royal Library (Copenhagen). |

2. Record Nr. UNIORUON00235394

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| Autore | FLUTRE, Louis-Fernand |
| Titolo | Table des nms propres avec toutes leurs variantes figurant dans les romans du Moyen Age écrits en Français ou en provençal et actuellement publiés ou analysés / Louis-Fernand Flutre |
| Pubbl/distr/stampa | Poitiers, : Centre d'Etudes Supérieures de Civilisation Médievale, 1962 |
| Descrizione fisica | XVI, 324 p. ; 28 cm. |
| Disciplina | 016 |
| Soggetti | BIBLIOGRAFIA FRANCESE - Sec. 18 |
| Lingua di pubblicazione | Francese |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |

3. Record Nr. UNINA9910767528503321

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| Titolo | Hardware and Software, Verification and Testing : Second International Haifa Verification Conference, HVC 2006, Haifa, Israel, October 23-26, 2006, Revised Selected Papers / / edited by Eyal Bin, Avi Ziv, Shmuel Ur |
| Pubbl/distr/stampa | Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2007 |
| ISBN | 1-280-86571-7<br>9786610865710<br>3-540-70889-8 |
| Edizione | [1st ed. 2007.] |
| Descrizione fisica | 1 online resource (XII, 235 p.) |
| Collana | Programming and Software Engineering, , 2945-9168 ; ; 4383 |
| Disciplina | 004 |
| Soggetti | Software engineering<br>Computer science<br>Compilers (Computer programs)<br>Software Engineering<br>Computer Science Logic and Foundations of Programming<br>Compilers and Interpreters |
| Lingua di pubblicazione | Inglese |
| Formato | Materiale a stampa |

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| Livello bibliografico | Monografia |
| Note generali | Bibliographic Level Mode of Issuance: Monograph |
| Nota di bibliografia | Includes bibliographical references and index. |
| Nota di contenuto | Hardware Verification Track -- Model Checking PSL Using HOL and SMV -- Using Linear Programming Techniques for Scheduling-Based Random Test-Case Generation -- Extracting a Simplified View of Design Functionality Based on Vector Simulation -- Automatic Fault Localization for Property Checking -- Verification of Data Paths Using Unbounded Integers: Automata Strike Back -- Tools Track -- Smart-Lint: Improving the Verification Flow -- Model-Driven Development with the jABC -- Detecting Design Flaws in UML State Charts for Embedded Software -- A Panel: Unpaved Road Between Hardware Verification and Software Testing Techniques -- An Open Source Simulation Model of Software Development and Testing -- Software Testing Track -- ExpliSAT: Guiding SAT-Based Software Verification with Explicit States -- Evolutionary Testing: A Case Study -- A Race-Detection and Flipping Algorithm for Automated Testing of Multi-threaded Programs -- Explaining Intermittent Concurrent Bugs by Minimizing Scheduling Noise -- Testing the Machine in the World -- Choosing a Test Modeling Language: A Survey -- Making Model-Based Testing More Agile: A Use Case Driven Approach. |
| Sommario/riassunto | The Haifa Verification Conference 2006 took place for the second year in a row at the IBM Haifa Research Lab and at the Haifa University in Israel during October 23–26, 2006. The verification conference was a three-day, single-track conference followed by a one-day tutorial on PSL. This Haifa Verification Conference was established to bring together researchers from two different disciplines, hardware verification and software testing. The use of similar techniques among the two communities enabled the conference to help generate a unique synergy that fortifies both groups. This year, we had two traditional tracks, hardware verification and software testing, in addition to a new track dedicated to tools in these areas. The conference emphasized applicability to real-world challenges, which was vital to the many attendees coming from industry. The conference hosted two internationally recognized individuals as keynote speakers. Randal E. Bryant, Dean and University Professor from the School of Computer Science at Carnegie Mellon University gave a talk on “System Modeling and Formal Verification with UCLID” and Michael Jackson from the University of Newcastle gave a talk on “Testing the Machine in the World. ” The numerous invited speakers presented topics of great interest to the audience. Just some of these outstanding speakers included Cindy Eisner in the hardware verification track, Alon Gluska and Andrew Piziali in the tools track, and Mauro Pezze and Nir Shavit in the software testing track. |