

1. Record Nr.	UNINA9910480405003321
Autore	Smith Steven W.
Titolo	Digital signal processing : a practical guide for engineers and scientists // by Steven W. Smith
Pubbl/distr/stampa	Amsterdam ; ; Boston : , : Newnes, , [2003] ©2003
ISBN	1-281-77196-1 9786611771966 0-08-047732-1
Edizione	[1st edition]
Descrizione fisica	1 online resource (666 p.)
Collana	Demystifying technology series
Disciplina	621.382/2 22 621.3822
Soggetti	Signal processing - Digital techniques Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	CD-ROM contains source code listings, problem sets, and an eBook version with full text search. Includes index.
Nota di contenuto	Cover; Frontmatter; Half Title Page; Title Page; Copyright; Contents at A Glance; Table of Contents; Preface; Chapter 1. The Breadth and Depth of DSP; The Roots of DSP; Telecommunications; Audio Processing; Echo Location; Image Processing; Chapter 2. Statistics, Probability and Noise; Signal and Graph Terminology; Mean and Standard Deviation; Signal vs. Underlying Process; The Histogram, Pmf and Pdf; The Normal Distribution; Digital Noise Generation; Precision and Accuracy; Chapter 3. ADC and DAC; Quantization; The Sampling Theorem; Digital-to- Analog Conversion Analog Filters for Data Conversion Selecting the Antialias Filter; Multirate Data Conversion; Single-Bit Data Conversion; Chapter 4. DSP Software; Computer Numbers; Fixed Point (Integers); Floating Point (Real Numbers); Number Precision; Execution Speed: Program Language; Execution Speed: Hardware; Execution Speed: Programming Tips; Chapter 5. Linear Systems; Signals and Systems; Requirements for Linearity; Static Linearity and Sinusoidal Fidelity; Examples of Linear

and Nonlinear Systems; Special Properties of Linearity; Superposition: The Foundation of DSP; Common Decompositions
Alternatives to Linearity
Chapter 6. Convolution; The Delta Function and Impulse Response; Convolution; The Input Side Algorithm; The Output Side Algorithm; The Sum of Weighted Inputs; Chapter 7. Properties of Convolution; Common Impulse Responses; Mathematical Properties; Correlation; Speed; Chapter 8. The Discrete Fourier Transform; The Family of Fourier Transform; Notation and Format of the Real DFT; The Frequency Domain's Independent Variable; DFT Basis Functions; Synthesis, Calculating the Inverse DFT; Analysis, Calculating the DFT; Duality; Polar Notation; Polar Nuisances
Chapter 9. Applications of the DFT
Spectral Analysis of Signals; Frequency Response of Systems; Convolution via the Frequency Domain; Chapter 10. Fourier Transform Properties; Linearity of the Fourier Transform; Characteristics of the Phase; Periodic Nature of the DFT; Compression and Expansion, Multirate Methods; Multiplying Signals (Amplitude Modulation); The Discrete Time Fourier Transform; Parseval's Relation; Chapter 11. Fourier Transform Pairs; Delta Function Pairs; The Sinc Function; Other Transform Pairs; Gibbs Effect; Harmonics; Chirp Signals; Chapter 12. The Fast Fourier Transform
Real DFT Using the Complex DFT
How the FFT Works; FFT Programs; Speed and Precision Comparisons; Further Speed Increases; Chapter 13. Continuous Signal Processing; The Delta Function; Convolution; The Fourier Transform; The Fourier Series; Chapter 14. Introduction to Digital Filters; Filter Basics; How Information Is Represented in Signals; Time Domain Parameters; Frequency Domain Parameters; High-Pass, Band-Pass and Band-Reject Filters; Filter Classification; Chapter 15. Moving Average Filters; Implementation by Convolution; Noise Reduction vs. Step Response; Frequency Response
Relatives of the Moving Average Filter

Sommario/riassunto

In addition to its thorough coverage of DSP design and programming techniques, Smith also covers the operation and usage of DSP chips. He uses Analog Devices' popular DSP chip family as design examples. Also included on the companion website is technical info on DSP processors from the four major manufacturers (Analog Devices, Texas Instruments, Motorola, and Lucent) and other DSP software.
*Covers all major DSP topics
*Full of insider information and shortcuts
*Basic techniques and algorithms explained without complex numbers

2. Record Nr.	UNISA996497167503316
Autore	WAKE, William <1657-1737>
Titolo	A second defence of the exposition of the doctrine of the Church of England : against the new exceptions of Monsieur de Meaux, Late Bishop of Condom, and his vindicator, the first part, in which the account that has been given of the Bishop of Meaux's Exposition, is fully vindicated .
Pubbl/distr/stampa	London, : Printed for Robert Chiswell ..., 1687
Descrizione fisica	Testo elettronico (PDF) (2 parti: ([18], XXII, 100, [2]; [14], 198, [2] p.)
Disciplina	283
Soggetti	Chiese anglicane
Lingua di pubblicazione	Inglese
Formato	Risorsa elettronica
Livello bibliografico	Monografia
Note generali	<p>La seconda parte ha data di impronta 1688</p> <p>Attribuito a William Wake. cfr. NUC prima del 1956</p> <p>L'opuscolo è in risposta a Jacques Benigne Bossuet's An exposition of the doctrine of the Catholic Church. Il suo vendicatore è Joseph Johnston che ha scritto A vendication of the Bishop of Condom's exposition of the dottrina of the Catholic Church.</p> <p>"La tavola di questa prima parte" (cioè sommario): p. [2]-[7]</p> <p>"I contenuti" (cioè sommario dei contenuti) per la seconda parte: p. [3]-[13]</p> <p>Errato: pag. 100 della prima parte, e p. [14] della seconda parte</p>