1. Record Nr. UNINA9910478868503321

Autore	Weiss Eyal
Titolo	Low-power and high-sensitivity magnetic sensors and systems / / Eyal Weiss, Roger Alimi
Pubbl/distr/stampa	Norwood, Massachusetts : , : Artech House, , [2019] [Piscataqay, New Jersey] : , : IEEE Xplore, , [2018]
ISBN	1-63081-244-7
Descrizione fisica	1 online resource (309 pages)
Collana	Artech house remote sensing series
Disciplina	538.72028
Soggetti	Magnetometers Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Sommario/riassunto	This comprehensive new resource analyzes sources of noise and clutter that magnetic sensing system developers encounter. This book guides practitioners in designing and building low noise and low power consumption magnetic measurement systems. Various examples of magnetic surveillance and survey systems are provided. This book enables system designers to obtain an all-inclusive spectral understanding of typical sources of noise and clutter present in the system and environment for each application, in order to successfully design stable and sensitive low power magnetic sensing devices. Detection and localization methods are explored, as well as deterministic and heuristics algorithms which are an integral part of any magnetic sensing system. This book is aimed to eliminate some of the "black magic" manipulations present during low noise magnetic measurements. The book meticulously describes, analyzes and quantifies the variables that affect low noise measurement systems. Readers are able to understand sources of measurements irregularities and how to effectively mitigate them. Moreover, this book also presents low power magnetometers and dedicated low noise sampling techniques.

2.

Record Nr.	UNISALENTO991001481119707536
Autore	Pajno, Alessandro
Titolo	La riforma del governo : commento ai Decreti legislativi n. 300 e n. 303 del 1999 sulla riorganizzazione della presidenza del consiglio e dei ministeri / a cura di Alessandro Pajno e Luisa Torchia
Pubbl/distr/stampa	Bologna : Il mulino, C2000
ISBN	8815076425
Descrizione fisica	542 p. ; 21 cm
Altri autori (Persone)	Torchia, Luisa
Disciplina	342.45
Soggetti	Governo - Riforma Italia Governo Riforma
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Segue: Appendici

3. Record Nr. UNINA9910484336903321

Titolo	Embedded Computer Systems: Architectures, Modeling, and Simulation : 6th International Workshop, SAMOS 2006, Samos, Greece, July 17-20, 2006, Proceedings / / edited by Stamatis Vassiliadis, Stephan Wong, Timo D. Hämäläinen
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2006
ISBN	3-540-36411-0
Edizione	[1st ed. 2006.]
Descrizione fisica	1 online resource (XV, 492 p.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 4017
Altri autori (Persone)	VassiliadisStamatis WongStephan <1973-> HamalainenTimo D
Disciplina	004.2/2
Soggetti	Computer systems Computers Microprocessors Computer architecture Computer networks Electronic digital computers - Evaluation Computer System Implementation Computer Hardware Processor Architectures Computer Communication Networks System Performance and Evaluation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Keynotes -- Reconfigurable Platform for Digital Convergence Terminals -- European Research in Embedded Systems -- System Design and Modeling -- Interface Overheads in Embedded Multimedia Software -- A UML Profile for Asynchronous Hardware Design -- Automated Distribution of UML 2.0 Designed Applications to a Configurable Multiprocessor Platform -- Towards a Transformation Chain Modeling Language -- Key Research Challenges for Successfully Applying MDD

Within Real-Time Embedded Software Development -- Domain-Specific Modeling of Power Aware Distributed Real-Time Embedded Systems -- Mining Dynamic Document Spaces with Massively Parallel Embedded Processors -- Efficient Automated Clock Gating Using CoDeL -- An Optimization Methodology for Memory Allocation and Task Scheduling in SoCs Via Linear Programming -- Wireless Sensor Networks -- Designing Wireless Sensor Nodes -- Design, Implementation, and Experiments on Outdoor Deployment of Wireless Sensor Network for Environmental Monitoring -- LATONA: An Advanced Server Architecture for Ubiquitous Sensor Network -- An Approach for the Reduction of Power Consumption in Sensor Nodes of Wireless Sensor Networks: Case Analysis of Mica2 -- Energy-Driven Partitioning of Signal Processing Algorithms in Sensor Networks -- Preamble Sense Multiple Access (PSMA) for Impulse Radio Ultra Wideband Sensor Networks -- Security in Wireless Sensor Networks: Considerations and Experiments -- On Security of PAN Wireless Systems -- Processor Design -- Code Size Reduction by Compiler Tuning -- Energy Optimization of a Multi-bank Main Memory -- Probabilistic Modelling and Evaluation of Soft Real-Time Embedded Systems -- Hybrid Functional and Instruction Level Power Modeling for Embedded Processors -- Low-Power, High-Performance TTA Processor for 1024-Point Fast Fourier Transform -- Software Pipelining Support for Transport Triggered Architecture Processors -- SAD Prefetching for MPEG4 Using Flux Caches -- Effects of Program Compression -- Integrated Instruction Scheduling and Fine-Grain Register Allocation for Embedded Processors -- Compilation and Simulation Tool Chain for Memory Aware Energy Optimizations -- A Scalable, Multi-thread, Multi-issue Array Processor Architecture for DSP Applications Based on Extended Tomasulo Scheme -- Reducing Execution Unit Leakage Power in Embedded Processors -- Memory Architecture Evaluation for Video Encoding on Enhanced Embedded Processors -- Advantages of Java Processors in Cache Performance and Power for Embedded Applications -- Dependable Computing -- CARROT – A Tool for Fast and Accurate Soft Error Rate Estimation -- A Scheduling Strategy for a Real-Time Dependable Organic Middleware -- Autonomous Construction Technology of Community for Achieving High Assurance Service -- Preventing Denial-of-Service Attacks in Shared CMP Caches -- Architectures and Implementations -- A Method for Router Table Compression for Application Specific Routing in Mesh Topology NoC Architectures -- Real-Time Embedded System for Rear-View Mirror Overtaking Car Monitoring -- Design of Asynchronous Embedded Processor with New Ternary Data Encoding Scheme -- Hardware-Based IP Lookup Using n-Way Set Associative Memory and LPM Comparator -- A Flash File System to Support Fast Mounting for NAND Flash Memory Based Embedded Systems -- Rescheduling for Optimized SHA-1 Calculation -- Software Implementation of WiMAX on the Sandbridge SandBlaster Platform -- High-Radix Addition and Multiplication in the Electron Counting Paradigm Using Single Electron Tunneling Technology -- Area, Delay, and Power Characteristics of Standard-Cell Implementations of the AESS-Box -- Embedded Sensor Systems -- Integrated Microsystems in Industrial Applications -- A Solid-State 2-D Wind Sensor -- Fault-Tolerant Bus System for Airbag Sensors and Actuators.

Sommario/riassunto

This book constitutes the refereed proceedings of the 6th International Workshop on Systems, Architectures, Modeling, and Simulation, SAMOS 2006, held in Samos, Greece on July 2006. The 47 revised full papers presented together with 2 keynote talks were thoroughly reviewed and selected from 130 submissions. The papers are organized in topical sections on system design and modeling, wireless sensor networks,

processor design, dependable computing, architectures and implementations, and embedded sensor systems.