1. Record Nr.   UNINA9910466604003321
   Autore   Remy Jean
   Titolo   Elisabeth de la Trinite / / Jean Remy
   Pubbl/distr/stampa   Bruyeres-le-Chatel : , : Nouvelle Cite, , [2018]
                         ©2018
   ISBN   2-37582-171-8
   Descrizione fisica   1 online resource (62 pages)
   Collana   Prier 15 jours avec
   Disciplina   610.922
   Soggetti   Saints
                         Electronic books.
   Lingua di pubblicazione Francese
   Formato   Materiale a stampa
   Livello bibliografico Monografia

1. Record Nr.   UNINA9910466604003321
   Autore   Remy Jean
   Titolo   Elisabeth de la Trinite / / Jean Remy
   Pubbl/distr/stampa   Bruyeres-le-Chatel : , : Nouvelle Cite, , [2018]
                         ©2018

2.	Record Nr.	UNINA9910790290803321
	Autore	Tocqueville Alexis de <1805-1859.>
	Titolo	Letters from America [[electronic resource] /] / Alexis de Tocqueville ; edited, translated, and with an introduction by Frederick Brown
	Pubbl/distr/stampa	New Haven, ; ; London, : Yale University Press, c2010
	ISBN	1-280-57124-1
		9786613600844
		0-300-15383-X
	Descrizione fisica	1 online resource (304 p.)
	Altri autori (Persone)	BeaumontGustave de <1802-1866.>
		BrownFrederick <1934->
	Disciplina	973
		973.5
	Soggetti	National characteristics, American - 19th century
		United States Description and travel Sources
		United States Social life and customs 1783-1865 Sources
		United States Politics and government 1783-1865 Sources
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Includes excerpts from traveling companion Gustave de Beaumont.
	Nota di bibliografia	Includes bibliographical references and index.
	Nota di contenuto	Frontmatter -- CONTENTS -- INTRODUCTION -- THE CROSSING -- IN NEW YORK -- UPSTATE NEW YORK AND WEST -- NEW ENGLAND -- FROM PHILADELPHIA TO NEW ORLEANS -- THE LAST LEG: F ROM NEW ORLEANS TO WASHINGTON AND NEW YORK -- APPENDIX -- INDEX
	Sommario/riassunto	Young Alexis de Tocqueville arrived in the United States for the first time in May 1831, commissioned by the French government to study the American prison system. For the next nine months he and his companion, Gustave de Beaumont, traveled and observed not only prisons but also the political, economic, and social systems of the early republic. Along the way, they frequently reported back to friends and family members in France. This book presents the first translation of the complete letters Tocqueville wrote during that seminal journey, accompanied by excerpts from Beaumont's correspondence that provide details or different perspectives on the places, people, and American life and attitudes the travelers encountered.These delightful letters provide an intimate portrait of the complicated, talented

Tocqueville, who opened himself without prejudice to the world of Jacksonian America. Moreover, they contain many of the impressions and ideas that served as preliminary sketches for Democracy in America, his classic account of the American democratic system that remains an important reference work to this day. Accessible, witty, and charming, the letters Tocqueville penned while in America are of major interest to general readers, scholars, and students alike.

3.	Record Nr.	UNINA9910377818103321
	Autore	Jain Saurabh
	Titolo	Adaptive Digital Circuits for Power-Performance Range beyond Wide Voltage Scaling : From the Clock Path to the Data Path / / by Saurabh Jain, Longyang Lin, Massimo Alioto
	Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2020
	ISBN	3-030-38796-8
	Edizione	[1st ed. 2020.]
	Descrizione fisica	1 online resource (XVI, 168 p. 113 illus., 107 illus. in color.)
	Disciplina	621.381
	Soggetti	Electronic circuits
		Computer engineering
		Internet of things
		Embedded computer systems
		Microprocessors
		Circuits and Systems
		Cyber-physical systems, IoT
		Processor Architectures
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Nota di contenuto	Introduction to wide voltage scaling, applications and challenges -- Reconfigurable microarchitectures down to pipestage and memory bank level -- Automated design flows and run-time optimization for reconfigurable microarchitectures -- Case studies of reconfigurable microarchitectures: accelerators, microprocessors and memories --

Reconfigurable clock networks, automated design flows, run-time optimization and case study -- Conclusion.

Sommario/riassunto	This book offers the first comprehensive coverage of digital design techniques to expand the power-performance tradeoff well beyond that allowed by conventional wide voltage scaling. Compared to conventional fixed designs, the approach described in this book makes digital circuits more versatile and adaptive, allowing simultaneous optimization at both ends of the power-performance spectrum. Drop-in solutions for fully automated and low-effort design based on commercial CAD tools are discussed extensively for processors, accelerators and on-chip memories, and are applicable to prominent applications (e.g., IoT, AI, wearables, biomedical). Through the higher power-performance versatility techniques described in this book, readers are enabled to reduce the design effort through reuse of the same digital design instance, across a wide range of applications. All concepts the authors discuss are demonstrated by dedicated testchip designs and experimental results. To make the results immediately usable by the reader, all the scripts necessary to create automated design flows based on commercial tools are provided and explained. Provides extensive coverage of the challenges and the key technologies enabling wide power-performance range in digital sub-systems (e.g., processors, memories, accelerators); Includes in-depth description of silicon-proven methodologies to design reconfigurable data path and clock path; Describes techniques for reconfigurable microarchitectures, down to the pipestage and the clock repeater level; Uses a highly interdisciplinary approach covering the circuit, the microarchitectural and the system levels of abstraction; Presents practical design examples and the related methodologies; Offers complementary design files and scripts, useful to replicate the presented developments and develop new designs.