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Sommario/riassunto	High-speed, power-efficient analog integrated circuits can be used as standalone devices or to interface modern digital signal processors and micro-controllers in various applications, including multimedia, communication, instrumentation, and control systems. New architectures and low device geometry of complementary metaloxidesemiconductor (CMOS) technologies have accelerated the movement toward system on a chip design, which merges analog

circuits with digital, and radio-frequency components. CMOS: Analog  
Integrated Circuits: High-Speed and Power-Efficient Design describe

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