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Collana	From Semi Classical Semiconductors to Novel Spintronic Devices
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Sommario/riassunto	""Dramatic developments in developing semiconductor device technology and nanotechnologies over the last decades has placed increasing demands on the fabrication and design of new electronic devices, It is now necessary to implement nanoscale gate geometries to achieve the highest performance standards in new devices. The need to understand and model the operation of nano-devices is, therefore, fundamental to future development and optimal design. Semiclassical Heterojunctions to Novel Spintronic Devices explains research related to new spintronic devices. This E-book: reports unique result