

1. Record Nr.	UNINA9910457699403321
Autore	Yiu Joseph
Titolo	The definitive guide to the ARM Cortex-M3 [[electronic resource] /] / Joseph Yiu
Pubbl/distr/stampa	Amsterdam ; ; Boston, : Newnes, c2007
ISBN	1-281-03935-7 9786611039356 0-08-055143-2
Edizione	[1st edition]
Descrizione fisica	1 online resource (380 p.)
Collana	Embedded technology series
Disciplina	004.16 004.256
Soggetti	Embedded computer systems Microprocessors Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Includes bibliographical references (p. xix) and index.
Nota di contenuto	Front Cover; The Definitive Guide to the ARM Cortex-M3; Copyright Page; Table of Contents; Foreword; Preface; Acknowledgments; Terms and Abbreviations; Conventions; References; Chapter 1 - Introduction; What Is the ARM Cortex-M3 Processor?; Background of ARM and ARM Architecture; A Brief History; Architecture Versions; Processor Naming; Instruction Set Development; The Thumb-2 Instruction Set Architecture (ISA); Cortex-M3 Processor Applications; Organization of This Book; Further Readings; Chapter 2 - Overview of the Cortex-M3; Fundamentals; Registers; R0 to R12: General-Purpose Registers R13: Stack PointersR14: The Link Register; R15: The Program Counter; Special Registers; Operation Modes; The Built-In Nested Vectored Interrupt Controller; Nested Interrupt Support; Vectored Interrupt Support; Dynamic Priority Changes Support; Reduction of Interrupt Latency; Interrupt Masking; The Memory Map; The Bus Interface; The Memory Protection Unit; The Instruction Set; Interrupts and Exceptions; Debugging Support; Characteristics Summary; High Performance; Advanced Interrupt-Handling Features; Low Power Consumption; System Features; Debug Supports; Chapter 3 - Cortex-M3 Basics

Registers General-Purpose Registers R0-R7; General-Purpose Registers R8-R12; Stack Pointer R13; Link Register R14; Program Counter R15; Special Registers; Program Status Registers (PSRs); PRIMASK, FAULTMASK, and BASEPRI Registers; The Control Register; Operation Mode; Exceptions and Interrupts; Vector Tables; Stack Memory Operations; Basic Operations of the Stack; Cortex-M3 Stack Implementation; The Two-Stack Model in the Cortex-M3; Reset Sequence; Chapter 4 - Instruction Sets; Assembly Basics; Assembler Language: Basic Syntax; Assembler Language: Use of Suffixes; Assembler Language: Unified Assembler Language Instruction List; Unsupported Instructions; Instruction Descriptions; Assembler Language: Moving Data; LDR and ADR Pseudo Instructions; Assembler Language: Processing Data; Assembler Language: Call and Unconditional Branch; Assembler Language: Decisions and Conditional Branches; Assembler Language: Combined Compare and Conditional Branch; Assembler Language: Conditional Branches Using IT Instructions; Assembler Language: Instruction Barrier and Memory Barrier Instructions; Assembly Language: Saturation Operations; Several Useful Instructions in the Cortex-M3 MSR and MRS; IF-THEN; CBZ and CBNZ; SDIV and UDIV; REV, REVH, and REVSH; RBIT; SXTB, SXTH, UXTB, and UXTH; BFC and BFI; UBFX and SBFX; LDRD and STRD; TBB and TBH; Chapter 5 - Memory Systems; Memory System Features Overview; Memory Maps; Memory Access Attributes; Default Memory Access Permissions; Bit-Band Operations; Advantages of Bit-Band Operations; Bit-Band Operation of Different Data Sizes; Bit-Band Operations in C Programs; Unaligned Transfers; Exclusive Accesses; Endian Mode; Chapter 6 - Cortex-M3 Implementation Overview; The Pipeline
A Detailed Block Diagram

Sommario/riassunto

This user's guide does far more than simply outline the ARM Cortex-M3 CPU features; it explains step-by-step how to program and implement the processor in real-world designs. It teaches readers how to utilize the complete and thumb instruction sets in order to obtain the best functionality, efficiency, and reuseability. The author, an ARM engineer who helped develop the core, provides many examples and diagrams that aid understanding. Quick reference appendices make locating specific details a snap! Whole chapters are dedicated to: Debugging using the new CoreSight technologyMi

2. Record Nr.	UNISA996503570603316
Autore	Illner Neele
Titolo	Aktiver Passivismus : Eine Form des Lebens und Schreibens in Robert Musils Roman »Der Mann ohne Eigenschaften«
Pubbl/distr/stampa	Bielefeld : , : transcript, , 2022 ©2022
ISBN	3-8394-6499-4
Edizione	[1st ed.]
Descrizione fisica	1 electronic resource (288 p.)
Collana	Lettre
Soggetti	Literary studies: general Literary theory
Lingua di pubblicazione	Tedesco
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Frontmatter -- Inhalt -- Vorwort -- Einleitung -- Die »Gelegenheit des Ausbruchs« (Kapitel-Analyse) -- Mit Möglichkeitssinn Bedeutung erfinden -- Tat, Verbrechen und halber Wahn -- Das Prinzip des unzureichenden Grundes, Motive und Statistik -- Lebendigkeit, auf Kosten von Erbe und Lebenserhalt -- Schreiben in Wirklichkeit -- Sprache der Liebe und Rückkehr zum Eros -- Resümee -- Aktiver Passivismus (Katalyse) -- Literaturverzeichnis
Sommario/riassunto	Aktiver Passivismus ist Handlungshemmung und spannungsgeladene Lösung - und er ist der Grund dafür, dass in Robert Musils »Mann ohne Eigenschaften« so wenig passiert. Neele Illner zeigt auf, wie sich mit diesem Konzept nicht nur Musils Roman neu lesen, sondern auch ein Begriff des rechten Lebens entwickeln lässt, welches Widersprüche vereint, ohne sie aufzuheben. Dabei erweist sich der aktive Passivismus als Thema, das zahlreiche Denker*innen des 20. und 21. Jahrhunderts - von Hannah Arendt bis Quentin Meillassoux - umtreibt. Als Form des Lebens und der Literatur vermag er den Krieg aufzuhalten und die geschwisterliche Liebe wiederzuentdecken.

3. Record Nr.	UNISA996390106403316
Autore	Shakespeare William <1564-1616.>
Titolo	The tragedie of King Richard the Third [[electronic resource]] : Contayning his treacherous plots against his brother Clarence: the pittifull murder of his innocent nephewes: his tyrannicall vsurpation: with the whole course of his detested life, and most deserued death. As it hath been lately acted by the Kings Maiesties Seruants. Newly augmented. By William Shake-speare
Pubbl/distr/stampa	London, : Printed by Thomas Purfoot, and are to be sold by Mathew Law, dwelling in Pauls Church-yard, at the signe of the Foxe, neere S. Austines gate, 1622
Descrizione fisica	[92] p
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	In verse. Signatures: A-L M ² . Running title reads: The tragedie of Richard the Third. Reproduction of the original in the Henry E. Huntington Library and Art Gallery.
Sommario/riassunto	eebo-0113