

1. Record Nr.	UNINA9910456637203321
Autore	Ashenden Peter J
Titolo	Digital design [[electronic resource]] : an embedded systems approach using Verilog // Peter J. Ashenden
Pubbl/distr/stampa	Burlington, : Morgan Kaufmann Amsterdam, : Elsevier, c2008
ISBN	1-283-39612-2 9786613396129 0-08-055311-7
Descrizione fisica	1 online resource (579 p.)
Disciplina	621.39/16
Soggetti	Embedded computer systems Verilog (Computer hardware description language) System design Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Front Cover
Sommario/riassunto	Digital Design: An Embedded Systems Approach Using Verilog provides a foundation in digital design for students in computer engineering, electrical engineering and computer science courses. It takes an up-to-date and modern approach of presenting digital logic design as an activity in a larger systems design context. Rather than focus on aspects of digital design that have little relevance in a realistic design context, this book concentrates on modern and evolving knowledge and design skills. Hardware description language (HDL)-based design and verification is emphasized--Veril