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3.6 Stability of Single-Stage AE Modulators
3.7 Multi-Stage AE Modulators ; 3.8 Multi-Bit AE Modulators
; 3.9 Hybrid AE Modulators
; 3.10 Adaptive AE Modulators ; 3.11 Band-Pass AE Modulators
; 3.12 Summary ;
Problems ; References
4. Single-Bit Single-Stage AE Modulators Modeling and Design
4.1 Introduction ; 4.2 Modeling of AE Modulators
; 4.3 NTF Characteristics ; 4.4 Stability of AE Modulators
; 4.5 Stability Criteria
; 4.6 Noise Transfer Function Determination
; 4.7 AE Modulator Assessment ; 4.8 Summary
Problems

Sommario/riassunto

This important book deals with the modeling and design of higher-order single-stage delta-sigma modulators. It provides an overview of the architectures, the quantizer models, the design techniques and the implementation issues encountered in the study of the delta-sigma modulators. A number of applications are discussed, with emphasis on use in the design of analog-to-digital converters and in frequency synthesis. The book is education- rather than research-oriented, containing numerical examples and unsolved problems. It is aimed at introducing the final-year undergraduate, the graduate stu
