1. Record Nr. UNINA9910453451203321 Autore Jacob Bruce **Titolo** Memory systems [[electronic resource]]: cache, DRAM, disk // Bruce Jacob, Spencer W. Ng, David T. Wang; with contributions by Samuel Rodriguez San Francisco, CA,: Morgan Kaufmann Pubbl/distr/stampa Oxford, : Elsevier Science [distributor], 2008 **ISBN** 1-322-46554-1 1-281-76645-3 9786611766450 0-08-055384-2 Edizione [1st edition] Descrizione fisica 1 online resource (1017 p.) Altri autori (Persone) NgSpencer W WangDavid Disciplina 004.5 004.5 22 Soggetti Computer storage devices Computer input-output equipment Electronic books. Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Note generali Description based upon print version of record. Nota di contenuto Front Cover; In Praise of Memory Systems: Cache, DRAM, Disk; Memory Systems Cache, DRAM, Disk; Copyright Page; Contents; Preface; Overview. On Memory Systems and Their Design; Ov.1 Memory Systems; Ov.2 Four Anecdotes on Modular Design; Ov.3 Cross-Cutting Issues; Ov.4 An Example Holistic Analysis; Ov.5 What to Expect; Part I. Cache: Chapter 1. An Overview of Cache Principles: 1.1 Caches, 'Caches,' and "Caches"; 1.2 Locality Principles; 1.3 What to Cache, Where to Put It, and How to Maintain It; 1.4 Insights and Optimizations; Chapter 2. Logical Organization 2.1 Logical Organization: A Taxonomy 2.2 Transparently Addressed Caches; 2.3 Non-Transparently Addressed Caches; 2.4 Virtual

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## Sommario/riassunto

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