

1. Record Nr.	UNINA9910437921603321
Titolo	UTLEON3 : exploring fine-grain multi-threading in FPGAs / / Martin Danek ... [et al.]
Pubbl/distr/stampa	New York, : Springer, 2013
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Descrizione fisica	1 online resource (228 p.)
Altri autori (Persone)	DanekMartin
Disciplina	621.395
Soggetti	Threads (Computer programs) Field programmable gate arrays
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	pt. 1. Programming interface -- pt. 2. Implementation.
Sommario/riassunto	This book describes a specification, microarchitecture, VHDL implementation and evaluation of a SPARC v8 CPU with fine-grain multi-threading, called micro-threading. The CPU, named UTLEON3, is an alternative platform for exploring CPU multi-threading that is compatible with the industry-standard GRLIB package. The processor microarchitecture was designed to map in an efficient way the data-flow scheme on a classical von Neumann pipelined processing used in common processors, while retaining full binary compatibility with existing legacy programs. Describes and documents a working SPARC v8, with fine-grain multithreading and fast context switch; Provides VHDL sources for the described processor; Describes a latency-tolerant framework for coupling hardware accelerators to microthreaded processor pipelines; Includes programming by example in the micro-threaded assembly language. .

2. Record Nr.	UNINA9911023676303321
Autore	Raineri, Salvatore
Titolo	La navigazione elettrica / di Salvatore Raineri
Pubbl/distr/stampa	Roma, : E. Loescher & c., 1885
Descrizione fisica	VIII, 158 p., [20] c. di tav. : ill. ; 19 cm.
Disciplina	797.125
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Collocazione	A MUSA 914
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