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Passive Components at High Frequencies"; "Key Ideas of MMLDE";
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Algorithms in the EMLDE Method"; "The Embedded SBDE Algorithm";
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"Self-Healing Circuits Using Statistical Element Selection"
Introduction"; "Process Variations"; "Systematic Variations";
"Random Variations"; "Mismatch Correction Methods"; "Statistical
Element Selection"; "Basis"; "Methodology"; "Comparator Array in
65nm Bulk CMOS Technology"; "Design Architecture"; "Testing
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65nm CMOS Process"; "Flash ADC Architecture"; "Comparator
Design"; "Measurement Results"; "Conclusion"; "References"
"Improving Design Feature Reuse in Analog Circuit Design through
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Introduction"; "Related Work"; "Circuit Synthesis Based on Concept
Comparison and Combination"; "Systematic Comparison of Analog
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Sommario/riassunto

Despite the fact that in the digital domain, designers can take full benefits of IPs and design automation tools to synthesize and design very complex systems, the analog designers' task is still considered as a 'handcraft', cumbersome and very time consuming process. Thus, tremendous efforts are being deployed to develop new design methodologies in the analog/RF and mixed-signal domains. This book collects 16 state-of-the-art contributions devoted to the topic of systematic design of analog, RF and mixed signal circuits. Divided in the two parts Methodologies and Techniques recent theories, synthesis techniques and design methodologies, as well as new sizing approaches in the field of robust analog and mixed signal design automation are presented for researchers and R/D engineers.
