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Autore	Martins Ricardo M. F
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Altri autori (Persone)	LourencoNuno C. C HortaNuno C. G
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Sommario/riassunto	This book presents an innovative methodology for the automatic generation of analog integrated circuits (ICs) layout, based on template descriptions and on evolutionary computational techniques. A design automation tool, LAYGEN II, was implemented to validate the proposed approach giving special emphasis to reusability of expert design knowledge and to efficiency on retargeting operations.