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Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction -- Digital-to-Analog Converters -- Modeling and Analysis of Performance Limitations in CS-DACs -- Design Techniques for High-Performance Intrinsic and Smart CS-DACs -- A Novel Digital Calibration Technique: Dynamic-Mismatch Mapping (DMM) -- An On-chip Dynamic-Mismatch Sensor Based on a Zero-IF Receiver -- Design Example -- Conclusions.
Sommario/riassunto	This book describes a novel digital calibration technique called dynamic-mismatch mapping (DMM) to improve the performance of digital to analog converters (DACs). Compared to other techniques, the DMM technique has the advantage of calibrating all mismatch errors without any noise penalty, which is particularly useful in order to meet the demand for high performance DACs in rapidly developing applications, such as multimedia and communication systems. · Discusses fundamental performance limitations of digital to analog converters and summarizes existing design/calibration techniques; · Introduces a novel digital calibration technique, called dynamic-mismatch mapping (DMM) to improve both static and dynamic performance of DACs; · Includes two state-of-the-art DAC design examples with in-depth discussion.