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Processing -- System Verilog Based SOC Verification Environment for Flash Memory -- Behavioral Modeling of LDO -- Regulated Cascode Preamplifier Based Frontend Readout ASIC "ANUSPARSH" for Resistive Plate Chamber Detector -- Fault Tolerant Reversible Logic for Combinational Circuits: A Survey -- A CMOS Standard Cell Based Time-to-Digital Converter -- Task Migration For 3x3 Tile Based NOC Architecture -- Design and Implementation of an Efficient Multiplier using Vedic Mathematics and Charge Recovery logic -- Error Detection and Correction in Embedded Memories using Cyclic Code -- Determining Standard Cell Drive Strength Based on On-Chip Load Assessment -- Design of 12-Bit Cyclic Vernier Ring Time-to-Digital Converter -- Adder Based Address Generation for Embedded MBIST -- Circuit design Methodologies for Test Power Reduction in Nano Scaled Technologies -- Study of continuous Non-linear FM Pulse Compression Technique -- Performance Evaluation of New Multi Level Spreading Codes For Synchronous DS CDMA Communication -- A Novel Method for Construction of Structured Regular LDPC Codes with Girth Twelve using Gray Code Representations -- Neuro-Curvelet Model for Efficient Image Compression using Vector Quantization -- A Novel Design of Narrowband Band-pass filters on PTFE Laminate using Radial Stubs -- The Design and Simulation of 0.5dB Noise-Figure RF Narrowband LNA -- Design & Development of DC-DC Converter System to Drive a PZT Stack Actuator -- Development of Technique for Making Ohmic Contacts to PEDOT-PSS Films -- A Comparative Study of Performance of Ring Resonator and Disk Resonator in Gas Sensing Applications -- Simulation and Optimization of Channel Mobility in High-k / Metal Gate Nano Scale MOSFETs -- An Improved Artificial Neural Network Based Emotion Classification System for Expressive Facial Images -- DSP-FPGA Based Parallel Architecture for Acquisition and Compression of Instrumented Pipeline Inspection Gauge Data in Real Time -- A Contourlet Transform-Based Versatile Watermarking Algorithm for Medical Images -- Application of Wavelet Shrinkage Denoising Method Based on Genetic Algorithm to Partial Discharge Signals -- Automatic Image Mosaicing using Discrete Cosine Transform -- Visual Cryptography for Color Images with Meaningful Shares using Image Fusion Technique -- Automatic Detection of Microaneurysms from Fundus Images using Morphological Operations -- AERB SG D-25 and IEC 60880 for Certification of Software in Safety Systems of Indian NPP -- Selective Rotation Based CORDIC Architecture for High Speed Applications -- Bio-Inspired Image Processing for Contour Enhancement and Fourier Spectrum Model for Orientation and Motion Detection -- Efficient Color Image Retrieval with Selective Relevance Feedback -- Nonlinear Dynamical Analysis of Speech Signals -- A Survey on Gaze Estimation Techniques -- Handwritten Script Recognition using DCT, Gabor Filter and Wavelet Features at Word Level -- Word Based LID using HMM and Bigram Modeling -- Discrimination of Handwritten and Machine Printed Text in Scanned Document Images -- Harmonic Analysis and its Elimination in Non Linear Loads using Wavelet Transform -- Lanczos Resampling for the Digital Processing of Remotely Sensed Images -- Performance Analysis of Adaptive DOA Estimation Algorithms for Mobile Applications -- Design of Security Schemes for Wireless Sensor Networks Based on Attack Behavior: Proactive Approach -- Ingress Flow Based Triple Token Bucket Traffic Control System for Distributed Networks -- Algorithm to find Minimal Cut-Sets of Complex Network -- IPv6 Based Network Performance metrics using Active Measurements -- A Survey of Virtualization Techniques in Cloud Computing.

keynote speakers and academicians in the International Conference on VLSI, Communication, Analog Designs, Signals and Systems, and Networking (VCASAN-2013), organized by B.N.M. Institute of Technology, Bangalore, India during July 17-19, 2013. The book provides global trends in cutting-edge technologies in electronics and communication engineering. The content of the book is useful to engineers, researchers and academicians as well as industry professionals.
