

1. Record Nr.	UNINA9910437766403321
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Titolo	An ASIC low power primer : analysis, techniques and specification / / Rakesh Chadha, J. Bhasker
Pubbl/distr/stampa	New York, : Springer, 2013
ISBN	1-283-93359-4 1-4614-4271-0
Edizione	[1st ed. 2013.]
Descrizione fisica	1 online resource (225 p.)
Altri autori (Persone)	BhaskerJ
Disciplina	621.395
Soggetti	Low voltage integrated circuits - Design and construction Application-specific integrated circuits
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction -- Modeling of Power in Core Logic -- Modeling of Power in IOS and Micro Blocks -- Power and Analysis in ASCIS -- Design Intent for Power Management -- Architectural Techniques for Low Power -- Low Power Implementation Techniques -- UPF Power Specification -- CPF Power Specification -- Appendix A -- Appendix B -- Bibliography -- Index. .
Sommario/riassunto	This book provides an invaluable primer on the techniques utilized in the design of low power digital semiconductor devices. Readers will benefit from the hands-on approach which starts from the ground-up, explaining with basic examples what power is, how it is measured and how it impacts on the design process of application-specific integrated circuits (ASICs). The authors use both the Unified Power Format (UPF) and Common Power Format (CPF) to describe in detail the power intent for an ASIC and then guide readers through a variety of architectural and implementation techniques that will help meet the power intent. From analyzing system power consumption, to techniques that can be employed in a low power design, to a detailed description of two alternate standards for capturing the power directives at various phases of the design, this book is filled with information that will give ASIC designers a competitive edge in low-power design. Starts from the ground-up and explains what power is, how it is measured and how it impacts on the ASIC design process; Provides essential information

in an easy to read and understand format, using basic examples; Explains what power intent is, how to describe it precisely and what techniques can be used to achieve the power intent with the two key standards, the Unified Power Format (UPF) and Common Power Format (CPF).
