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| Descrizione fisica | 1 online resource (IX, 139 p. 74 illus., 67 illus. in color.) |
| Disciplina | 005.8 |
| Soggetti | Electronic circuits Computer engineering Internet of things Embedded computer systems Microprocessors Circuits and Systems Cyber-physical systems, IoT Processor Architectures |
| Lingua di pubblicazione | Inglese |
| Formato | Materiale a stampa |
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| Note generali | Includes index. |
| Nota di contenuto | Introduction -- Practical Split Manufacturing Optimization -- IC Camouflaging Optimization and Evaluation -- Fault Attack Protection and Evaluation -- Conclusion. |
| Sommario/riassunto | This book proposes a synergistic framework to help IP vendors to protect hardware IP privacy and integrity from design, optimization, and evaluation perspectives. The proposed framework consists of five interacting components that directly target at the primary IP violations. All the five algorithms are developed based on rigorous mathematical modeling for primary IP violations and focus on different stages of IC design, which can be combined to provide a formal security guarantee. |