1. Record Nr. UNINA9910366591903321 Autore Tlelo-Cuautle Esteban Titolo Analog/Digital Implementation of Fractional Order Chaotic Circuits and Applications [[electronic resource] /] / by Esteban Tlelo-Cuautle, Ana Dalia Pano-Azucena, Omar Guillén-Fernández, Alejandro Silva-Juárez Cham:,: Springer International Publishing:,: Imprint: Springer,, Pubbl/distr/stampa 2020 3-030-31250-X ISBN Edizione [1st ed. 2020.] 1 online resource (XVI, 212 p. 124 illus., 88 illus. in color.) Descrizione fisica Disciplina 621.3815 Soggetti Electronic circuits Cooperating objects (Computer systems) Electronics Electronic Circuits and Systems Cyber-Physical Systems Electronics and Microelectronics, Instrumentation Lingua di pubblicazione **Formato** Materiale a stampa Livello bibliografico Monografia Integer and Fractional Chaotic Circuits and Systems -- Characteristics Nota di contenuto of chaotic systems -- Simulation and optimization of fractional-order chaotic systems -- Analog implementations of fractional-order chaotic systems -- Digital implementations of fractional-order chaotic systems -- Synchronization and applications of fractional-order chaotic systems -- Conclusions. Sommario/riassunto This book details the simulation and optimization of integer and fractional-order chaotic systems, and how they can be implemented in the analog and digital domains using FPAAs and FPGAs. Design guidelines are provided to use commercially available electronic devices, and to perform hardware descriptions of integer/fractionalorder chaotic systems programming in VHDL. Finally, several engineering applications oriented to cryptography, internet of things, robotics and chaotic communications, are detailed to highlight the

usefulness of FPAA/FPGA based integer/fractional-order chaotic systems. Provides guidelines to implement fractional-order derivatives

using commercially available devices; Describes details on using FPAAs to approach fractional-order chaotic systems; Includes details on using FPGAs to approach fractional-order chaotic systems, programming in VHDL and reducing hardware resources; Discusses applications to cryptography, internet of things, robotics and chaotic communications.