Record Nr. UNINA9910366582003321 Next-Generation ADCs, High-Performance Power Management, and **Titolo** Technology Considerations for Advanced Integrated Circuits: Advances in Analog Circuit Design 2019 / / edited by Andrea Baschirotto, Pieter Harpe, Kofi A. A. Makinwa Pubbl/distr/stampa Cham:,: Springer International Publishing:,: Imprint: Springer,, 2020 **ISBN** 3-030-25267-1 Edizione [1st ed. 2020.] 1 online resource (322 pages) Descrizione fisica Disciplina 621.39814 Soggetti Electronic circuits Computer engineering Internet of things Embedded computer systems Circuits and Systems Cyber-physical systems, IoT **Electronic Circuits and Devices** Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Nota di bibliografia Includes bibliographical references and index. Nota di contenuto Part I. Next-Generation ADCs -- Chapter 1. Emerging ADCs -- Chapter 2. Noise-Shaping SAR ADCs -- Chapter 3. Efficient High-Resolution Nyquist ADCs -- Chapter 4. Continous-Time ADCs for Automative Applications -- Chapter 5. Continuous-Time Delta-Sigma Converters

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Sommario/riassunto

This book is based on the 18 tutorials presented during the 28th workshop on Advances in Analog Circuit Design. Expert designers present readers with information about a variety of topics at the frontier of analog circuit design, including next-generation analog-to-digital converters , high-performance power management systems and technology considerations for advanced IC design. For anyone involved in analog circuit research and development, this book will be a valuable summary of the state-of-the-art in these areas. Provides a summary of the state-of-the-art in analog circuit design, written by experts from industry and academia; Presents material in a tutorial-based format; Includes coverage of next-generation analog-to-digital converters, high-performance power management systems, and technology considerations for advanced IC design.