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Titolo	Handbook of Memristor Networks / / edited by Leon Chua, Georgios Ch. Sirakoulis, Andrew Adamatzky
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Descrizione fisica	1 online resource (XIV, 1368 p. 790 illus., 615 illus. in color.)
Disciplina	004
Soggetti	Computer hardware Computers Electronic circuits Computer Hardware Theory of Computation Circuits and Systems
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	The Fourth Element Aftermath of Finding the Memristor Three Fingerprints of Memristor Resistance Switching Memories Are Memristors The Detectors Used in the First Radios Were Memristors Why Are Memristor and Memistor Different Devices? The Art and Science of Constructing a Memristor Model: Updated Memristor, Hodgkin-Huxley, and Edge of Chaos Brains Are Made of Memristors Synapse as a Memristor Memristors and Memristive Devices for Neuromorphic Computing Bio-inspired Neural Networks Self- organization and Emergence of Dynamical Structures in Neuromorphic Atomic Switch Networks Spike-Timing-Dependent-Plasticity with Memristors Designing Neuromorphic Computing Systems with Memristor Devices Brain-inspired Memristive Neural Networks for Unsupervised Learning Neuromorphic Devices and Networks Based on Memristors with Ionic Dynamics Memristor Bridge-Based Artificial Neural Weighting Circuit Cellular Nonlinear Networks with Memristor Synapses Evolving Memristive Neural Networks Behavior of Multiple Memristor Circuits A Memristor-Based Chaotic System with

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	Boundary Conditions Associative networks and perceptron based on memristors: fundamentals and algorithmic implementation Spiking Neural Computing in Memristive Neuromorphic Platforms Spiking in Memristor Networks Organic Memristive Devices and Neuromorphic Circuits Associative Enhancement and its Application in Memristor based Neuromorphic Devices Three-dimensional Crossbar of Self- rectifying Si/SiO2/Si Memristors The Self-Directed Channel Memristor: Operational Dependence on the Metal-Chalcogenide Layer Memristive in Situ Computing A Taxonomy and Evaluation Framework for Memristive Logic Memristive Stateful Logic Memory Effects in Multi-terminal Solid State Devices and Their Applications Memristor-Based Addition and Multiplication Memristor Emulators Switching Synchronization and Metastable States in 1D Memristive Networks Modeling Memristor-Based Circuit Networks on Crossbar Architectures Computing Shortest Paths in 2D and 3D Memristive Networks Computing Image and Motion with 3-D Memristive Grids Solid-State Memcapacitors and Their Applications Reaction-Diffusion Media with Excitable Oregonators Coupled by Memristors Mimicking Physarum Space Exploration with Networks of Memristive Oscillators Autowaves in a Lattice of Memristor-Based
Sommario/riassunto	This Handbook presents all aspects of memristor networks in an easy to read and tutorial style. Including many colour illustrations, it covers the foundations of memristor theory and applications, the technology of memristive devices, revised models of the Hodgkin-Huxley Equations and ion channels, neuromorphic architectures, and analyses of the dynamic behaviour of memristive networks. It also shows how to realise computing devices, non-von Neumann architectures and provides future building blocks for deep learning hardware. With contributions from leaders in computer science, mathematics, electronics, physics, material science and engineering, the book offers an indispensable source of information and an inspiring reference text for future generations of computer scientists, mathematicians, physicists, material scientists and engineers working in this dynamic field.