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Disciplina	004.015113 006.333
Soggetti	Software engineering Compilers (Computer programs) Data protection Computer simulation Computer science Machine theory Software Engineering Compilers and Interpreters Data and Information Security Computer Modelling Computer Science Logic and Foundations of Programming Formal Languages and Automata Theory
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
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Note generali	Includes index.
Nota di contenuto	Formal Methods -- Verification -- Program Synthesis -- Model Checking -- Theorem Proving -- Software Verification -- SAT Solver -- SMT Solver -- Automation Logic.
Sommario/riassunto	This book constitutes the refereed proceedings of the 16th International Symposium on Automated Technology for Verification and Analysis, ATVA 2018, held in Los Angeles, CA, USA in October 2018. The 27 full papers presented together with 5 short papers and 3 invited

talks were carefully reviewed and selected from 82 submissions. The symposium is dedicated to the promotion of research on theoretical and practical aspects of automated analysis, verification and synthesis by providing a forum for interaction between the regional and the international research communities and industry in the field.
