

1. Record Nr.	UNINA9910349358103321
Titolo	62050-2005 - IEC/IEEE international standard - VHDL Register Transfer Level (RTL) synthesis // Institute of Electrical and Electronics Engineers
Pubbl/distr/stampa	New York, New York : , : IEEE, , 2005
ISBN	0-7381-4779-6
Descrizione fisica	1 online resource (128 pages)
Disciplina	621.392
Soggetti	VHDL (Computer hardware description language)
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Sommario/riassunto	Replaces IEEE Std 1076.6-2004. This document specifies a standard for use of very high-speed integrated circuit hardware description language (VHDL) to model synthesizable register-transfer level digital logic. A standard syntax and semantics for VHDL register-transfer level synthesis is defined. The subset of the VHDL language, which is synthesizable, is described, and nonsynthesizable VHDL constructs are identified that should be ignored or flagged as errors.