1. Record Nr.   UNINA9910790078003321

   Autore   Lu Xin Biao

   Titolo   Synchronization in complex networks [[electronic resource] /] / Xin Biao
                          Lu and Bu Zhi Qin

   Pubbl/distr/stampa   New York, : Nova Science Publisher's, c2011

   ISBN   1-61122-260-5

   Descrizione fisica   1 online resource (147 p.)

   Collana   Computer networks.

   Altri autori (Persone) QinBu Zhi

   Disciplina   004.6/5

   Soggetti   Synchronous data transmission systems
                          Computer network architectures

   Lingua di pubblicazione Inglese

   Formato   Materiale a stampa

   Livello bibliografico Monografia

   Note generali   Description based upon print version of record.

   Nota di bibliografia   Includes bibliographical references (p. [123]-133) and index.

2.	Record Nr.	UNINA9910337649903321
	Titolo	Approximate Circuits : Methodologies and CAD / / edited by Sherief Reda, Muhammad Shafique
	Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2019
	ISBN	3-319-99322-4
	Edizione	[1st ed. 2019.]
	Descrizione fisica	1 online resource (495 pages)
	Disciplina	005.1
	Soggetti	Electronic circuits Microprocessors Electronics Microelectronics Circuits and Systems Processor Architectures Electronics and Microelectronics, Instrumentation
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Nota di contenuto	Introduction -- Building Blocks -- General Data-Path Circuit-Level / Design Methods -- Memories & I/O -- System-Level -- Synergistic compiler and programming languages -- Conclusion.
	Sommario/riassunto	This book provides readers with a comprehensive, state-of-the-art overview of approximate computing, enabling the design trade-off of accuracy for achieving better power/performance efficiencies, through the simplification of underlying computing resources. The authors describe in detail various efforts to generate approximate hardware systems, while still providing an overview of support techniques at other computing layers. The book is organized by techniques for various hardware components, from basic building blocks to general circuits and systems. Presents an overview of the approximate arithmetic building blocks that can be used for designing power/performance efficient computing units; Discusses effective memory approximation techniques to employ in conventional, i.e., DRAM and SRAM, as well as emerging, i.e., PCM and STT-RAM, memory

technologies, for improving performance, power, and/or energy efficiency of the memory for error resilient applications; Includes an overview of hardware or software/hardware approximation techniques that operate across entire computing devices, including processors, graphical processors, and accelerators that can form a SoC with processors.