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| Soggetti | Compilers (Computer programs) Logic design Computer input-output equipment Microprogramming Computer engineering Computer networks Compilers and Interpreters Logic Design Input/Output and Data Communications Control Structures and Microprogramming Computer Engineering and Networks |
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| Sommario/riassunto | This book constitutes the refereed post-conference proceedings of the 5th International Workshop on Accelerator Programming Using Directives, WACCPD 2018, held in Dallas, TX, USA, in November 2018. The 6 full papers presented have been carefully reviewed and selected from 12 submissions. The papers share knowledge and experiences to program emerging complex parallel computing systems. They are organized in the following three sections: applications; using openMP; and program evaluation. |

