1. Record Nr. UNISA990000888820203316

Autore	RISTUCCIA, Renzo
Titolo	Il software nella dottrina, nella giurisprudenza e nel D. LGS. 518-1992 : con 65 decisioni di giudici italiani / Renzo Ristuccia, Vincenzo Zeno-Zencovich
Pubbl/distr/stampa	Padova : Cedam, 1993
ISBN	88-13-18293-7
Edizione	[2. ed]
Descrizione fisica	X, 319 p. ; 24 cm
Collana	DLST
Altri autori (Persone)	ZENO-ZENCOVICH, Vincenzo
Disciplina	346.450482
Soggetti	Elaboratori elettronici - Programmi - Tutela giuridica - Giurisprudenza
Collocazione	COLL. HPG 1 BIS XXII.1. Coll. 16/ 2 (COLL. HPG 1 BIS)
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

2. Record Nr. UNINA9910150217103321

Autore	Jaramillo Susie
Titolo	Little chickies = : Los pollitos / / Susie Jaramillo
Pubbl/distr/stampa	[New York] : , : Encantos, , [2016]
ISBN	0-9969959-5-1
Descrizione fisica	1 online resource (25 pages) : color illustrations
Collana	Canticos
Disciplina	863.7
Soggetti	Chicks Chickens Hens Picture books for children
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia

3. Record Nr. UNINA9910300423603321

Autore	Hehn Thorsten
Titolo	CMOS Circuits for Piezoelectric Energy Harvesters : Efficient Power Extraction, Interface Modeling and Loss Analysis / / by Thorsten Hehn, Yiannos Manoli
Pubbl/distr/stampa	Dordrecht : , : Springer Netherlands : , : Imprint : Springer, , 2015
ISBN	94-017-9288-7
Edizione	[1st ed. 2015.]
Descrizione fisica	1 online resource (217 p.)
Collana	Springer Series in Advanced Microelectronics, , 1437-0387 ; ; 38
Disciplina	621.3815
Soggetti	Electronic circuits Energy harvesting Power electronics Electronic Circuits and Devices Energy Harvesting Circuits and Systems Power Electronics, Electrical Machines and Networks
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references at the end of each chapters and index.
Nota di contenuto	1 Introduction -- 2 Piezoelectricity and Energy Harvester Modelling -- 3 Analysis of Different Interface Circuits -- 4 Theory of the Proposed PSCE Circuit -- 5 Implementation of the PSCE Circuit on Transistor Level -- 6 Performance Analysis of the PSCE Chip -- 7 Conclusions and Outlook. References -- Appendix A Mathematical Calculations -- A.1 Solution of the Linear Differential Equation Systems -- A.2 Flux Property -- A.3 Trigonometric Relations -- A.4 Numerical Calculation.
Sommario/riassunto	This book deals with the challenge of exploiting ambient vibrational energy which can be used to power small and low-power electronic devices, e.g. wireless sensor nodes. Generally, particularly for low voltage amplitudes, low-loss rectification is required to achieve high conversion efficiency. In the special case of piezoelectric energy harvesting, pulsed charge extraction has the potential to extract more power compared to a single rectifier. For this purpose, a fully autonomous CMOS integrated interface circuit for piezoelectric

generators which fulfills these requirements is presented. Due to these key properties enabling universal usage, other CMOS designers working in the field of energy harvesting will be encouraged to use some of the shown structures for their own implementations. The book is unique in the sense that it highlights the design process from scratch to the final chip. Hence, it gives the designer a comprehensive guide of how to (i) setup an appropriate harvester model to get realistic simulation results, (ii) design the integrated circuits for low power operation, (iii) setup a laboratory measurement environment in order to extensively characterize the chip in combination with the real harvester, and finally, (iv) interpret the simulation/measurement results in order to improve the chip performance. Since the dimensions of all devices (transistors, resistors etc.) are given, readers and other designers can easily re-use the presented circuit concepts.