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Nota di contenuto	Foreword -- Preface -- Part I Application Background and Energy Harvester Interfacing -- 1 Introduction -- 2 Basic Transducer Interfacing Concepts -- Part II Circuits and Functional Blocks -- 3 Low-Voltage CMOS Design Fundamentals -- 4 0.5-V Low-Power Analog Circuits -- Part III Prototype Development and Circuit Integration -- 5 Low-Voltage Rectification of High-resistive Sources -- 6 Input Load Adapting Charge Pump Interface -- 7 Load Matching Detector -- 8 Switched-Inductor Capacitive Interface -- 9 Conclusion and Future Aspects -- A Application Load Profile -- B Electromagnetic Transducer - Model and Properties -- C MPP Tracking - Power Transfer Timing -- D MOS Devices and XH035 Process -- E Circuit Blocks Implementation Tables -- F AC-DC Rectification -- G Load Matching Detector -- H Switched Inductor Capacitive Interface -- I Source Code Listings and Models -- Index.
Sommario/riassunto	Chip-integrated power management solutions are a must for ultra-low power systems. This enables not only the optimization of innovative

sensor applications. It is also essential for integration and miniaturization of energy harvesting supply strategies of portable and autonomous monitoring systems. The book particularly addresses interfaces for energy harvesting, which are the key element to connect micro transducers to energy storage elements. Main features of the book are: - A comprehensive technology and application review, basics on transducer mechanics, fundamental circuit and control design, prototyping and testing, up to sensor system supply and applications. - Novel interfacing concepts - including active rectifiers, MPPT methods for efficient tracking of DC as well as AC sources, and a fully-integrated charge pump for efficient maximum AC power tracking at sub-100 $\mu$ W ultra-low power levels. The chips achieve one of widest presented operational voltage range in standard CMOS technology: 0.44V to over 4.1V. - Two special chapters on analog circuit design – it studies benefits and obstacles on implemented chip prototypes with three goals: ultra- low power, wide supply voltage range, and integration with standard technologies. Alternative design approaches are pursued using bulk-input transistor stages in forward-bias operation for amplifiers, modulators, and references. - Comprehensive Appendix – with additional fundamental analysis, design and scaling guidelines, circuit implementation tables and dimensions, schematics, source code listings, bill of material, etc. The discussed prototypes and given design guidelines are tested with real vibration transducer devices. The intended readership is graduate students in advanced courses, academics and lecturers, R&D engineers.

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