

1. Record Nr.	UNINA9910299942703321
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Titolo	Advanced Techniques for Power, Energy, and Thermal Management for Clustered Manycores // by Santiago Pagani, Jian-Jia Chen, Muhammad Shafique, Jörg Henkel
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2018
ISBN	3-319-77479-4
Edizione	[1st ed. 2018.]
Descrizione fisica	1 online resource (295 pages) : illustrations
Disciplina	004.35
Soggetti	Electronic circuits Microprocessors Electronics Microelectronics Circuits and Systems Processor Architectures Electronics and Microelectronics, Instrumentation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction -- Background and Related Work -- System Model -- Experimental Framework -- Thermal Safe Power (TSP) -- Transient and Peak Temperature Computation based on Matrix Exponentials (MatEx) -- Selective Boosting for Multicore Systems (seBoost) -- Energy and Peak Power Efficiency Analysis for Simple Approximation Schemes -- Energy-Efficient Task-to-core Assignment for Homogeneous Clustered Manycores -- Energy-Efficient Task-to-core Assignment for Heterogeneous Clustered Manycores -- Conclusions.
Sommario/riassunto	This book focuses on two of the most relevant problems related to power management on multicore and manycore systems. Specifically, one part of the book focuses on maximizing/optimizing computational performance under power or thermal constraints, while another part focuses on minimizing energy consumption under performance (or real-time) constraints. Provides a comprehensive introduction to energy, power, and temperature management, highlighting the

different optimization goals, particularly computational performance, power and energy consumption, and temperature; Highlights the differences and similarities between the two key challenges of performance optimization under power or thermal constraints and energy minimization under performance constraints; Discusses in detail several means that can be used to optimize performance or energy while satisfying the desired constraints, including core heterogeneity, task-to-core assignment/mapping, dynamic power management (DPM), and dynamic voltage and frequency scaling (DVFS).
