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Nota di contenuto	Front-End CMOS Circuit for Low Power Consumption -- A Preliminary Study of Oscillators, Phase and Frequency Detector and Charge Pump for Phase Locked Loop (PLL) Applications -- All Digital RF Transmitter with Highly Power Efficient Doherty Power Amplifier -- Adiabatic Techniques for Energy Efficient Barrel Shifter Design -- A 79Ghz CMOS LNA with Adaptive Biasing -- X-Band Phased-Array Transmitter in 180nm Sige Bicmos Technology with Stacked Power Amplifier -- Short Range Low Data Rate Pulsed UWB Transmitter -- A New High Speed Multiplier Based on Carry Look Ahead Adder and Compressor -- Real-Time Automatic Peaks and Onsets Detection of Photoplethysmographic Signals -- Survey on Design of Low Power CMOS Four Quadrant Analog Multiplier in Nano-Meter Scaling -- Designing 5t Embedded Dram Cell for Ultra Low Power Low Voltage Applications Based on Schmitt Trigger -- Design and Implementation of Multi-Bit Self-Checking Carry-Select Adder -- A Novel Adiabatic Logic for Low Power VLSI Circuit Design and Power Optimization Using Finfet -- Test Signal Generation for Detecting Faults On Mil-Std 1553 Bus -- Design of Ultra Low Voltage-Energy Efficient Hybrid Full Adder Circuit -- Mtcmos Based Soft Start

Circuit for Low Leakage Led Driver with Minimum In-Rush Current --  
Implementation of Dual-Hysteresis Mode Flip-Flop  
Multivibrator Using Differential Voltage Current Conveyor -- High  
Performance Domino Logic Circuit Design By Contention Reduction --  
Differential Power Analysis (Dpa) Resistant Cryptographic S-Box --  
Implementation of Radix-2 Butterfly Using Distributed Arithmetic  
Algorithm (Daa) -- An Area Efficient Design of Warped Filters -- Design  
of Arithmetic and Logical Unit (Alu) Using Subthreshold Adiabatic Logic  
for Low Power Application -- Design of Sample and Hold for High  
Speed Analog To Digital Converter -- Fpga Masked S-Box  
Implementation for Aes Engine -- Pipelined and Parallel Architecture of  
Reversible Watermarking for Grayscale Images -- Design and  
Verification of Amba Axi3 Protocol -- Design of Multi-Stage Cmos Ota  
for Low-Power and High Driving Capability.

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### Sommario/riassunto

This book gathers a collection of papers by international experts presented at the International Conference on NextGen Electronic Technologies (ICNETS2-2017), which cover key developments in the field of electronics and communication engineering. ICNETS2 encompassed six symposia covering all aspects of the electronics and communications domains, including relevant nano/micro materials and devices. This book showcases the latest research in very-large-scale integration (VLSI) Design: Circuits, Systems and Applications, making it a valuable resource for all researchers, professionals, and students working in the core areas of electronics and their applications, especially in digital and analog VLSI circuits and systems.

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