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	Titolo	High-Performance AD and DA Converters, IC Design in Scaled Technologies, and Time-Domain Signal Processing [[electronic resource]] : Advances in Analog Circuit Design 2014 / / edited by Pieter Harpe, Andrea Baschirotto, Kofi A. A. Makinwa
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	Edizione	[1st ed. 2015.]
	Descrizione fisica	1 online resource (419 p.)
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	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Description based upon print version of record.
	Nota di bibliografia	Includes bibliographical references at the end of each chapters.
	Nota di contenuto	Part I High-Performance AD and DA Converters -- Low-power, High-speed and High-effective Resolution Pipeline Analog-to-digital Converters in Deep Nanoscale CMOS -- Digitally assisted analog to digital converters -- Energy-efficient high-speed SAR ADCs in CMOS -- Automated design of high-speed CT modulators employing compensation and correction of non-idealities -- Recent advances and trends in high-performance embedded data converters -- High-performance DACs: unifying 16-bit dynamic range with GS/s data-rates.
	Sommario/riassunto	This book is based on the 18 tutorials presented during the 23rd workshop on Advances in Analog Circuit Design. Expert designers present readers with information about a variety of topics at the frontier of analog circuit design, serving as a valuable reference to the

state-of-the-art, for anyone involved in analog circuit research and development. • Includes coverage of high-performance analog-to-digital and digital to analog converters, integrated circuit design in scaled technologies, and time-domain signal processing; • Provides a state-of-the-art reference in analog circuit design, written by experts from industry and academia; • Presents material in a tutorial-based format.