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Soggetti	Signal processing Computer architecture
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Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction -- Digital Transmission System -- Channel Coding Basics -- Hardware Design of Individual Components -- Turbo Codes -- Low-Density Parity Check Codes -- Bit-Interleaved Coded MIMO System -- Comparing Architectures.
Sommario/riassunto	This book addresses readers in electrical engineering, computer engineering, and computer science with an interest in the field of communications engineering, architectures, and microelectronic design. The focus is on implementation aspects and implementation constraints of individual components that are needed in transceivers for current standards, such as UMTS, LTE, WiMAX and DVB-S2. The application domain is the so called outer receiver, which comprises the channel coding, interleaving stages, modulator, and multiple antenna transmission. Throughout the book, the focus is on advanced algorithms that are actually in use in modern communications systems. Their basic principles are always derived with a focus on the resulting communications and implementation performance. As a result, this book serves as a valuable reference for two, typically disparate audiences in communication systems and hardware design. It addresses challenges faced by both the algorithm designer and the chip designer, who need to deal with the ongoing increase of

algorithmic complexity and required data throughput for today's mobile applications. • Enables algorithm designers and chip designers to meet challenges posed by the ongoing increase of algorithmic complexity and required data throughput for today's mobile applications; • Describes techniques for improved utilization of the bandwidth, as well as maximizing area and power profile to reduce fabrication costs; • Focuses on advanced algorithms that are actually in use in modern communications systems.
