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Nota di contenuto	A Power-optimized High-speed and High-resolution Pipeline ADC with a Parallel Sampling First Stage for Broadband Multi-carrier Systems -- Design of Power, Dynamic Range and Noise Scalable ADCs -- Current and Emerging Trends in the Design of Digital-to-Analog Converters -- Digitally-Based Calibration Techniques for RF Modulators -- Incremental and Extended-range Data Converters -- Event-Driven Successive Charge Redistribution Schemes for Clockless Analog-to-Digital Conversion.
Sommario/riassunto	This book presents the a scientific discussion of the state-of-the-art techniques and designs for modeling, testing and for the performance analysis of data converters. The focus is put on sustainable data conversion. Sustainability has become a public issue that industries and users can not ignore. Devising environmentally friendly solutions for data conversion designing, modeling and testing is nowadays a requirement that researchers and practitioners must consider in their activities. This book presents the outcome of the IWADC workshop 2011, held in Orvieto, Italy.