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Nota di contenuto	Part I Applications of Formal Methods for Specification and Verification -- Optimal Component Selection for Energy-Efficient Systems -- Assisting Refinement in System-on-Chip Design -- Part II Embedded Analog and Mixed-Signal System Verification -- Integrating Circuit Analyses for Assertion-Based Verification of Programmable AMS Circuits -- Part III Embedded Analog and Mixed-Signal System Design -- Hybrid Dynamical Systems for Memristor Modelling -- Code Generation Alternatives to Reduce Heterogeneous Embedded Systems Homogeneity.
Sommario/riassunto	This book brings together a selection of the best papers from the sixteenth edition of the Forum on specification and Design Languages Conference (FDL), which was held in September 2013 in Paris, France. FDL is a well-established international forum devoted to dissemination of research results, practical experiences and new ideas in the application of specification, design and verification languages to

the design, modeling and verification of integrated circuits, complex hardware/software embedded systems, and mixed-technology systems. • Covers applications of formal methods for specification, verification and debug; • Includes embedded analog and mixed-signal system design; • Enables model-driven engineering for embedded systems design and development.

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