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Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction.- Power Amplifier Fundamentals: Metrics -- Power Amplifier Fundamentals: Classes -- CMOS Performance Issues -- Enhancement Techniques for CMOS Linear PAs -- Power Amplifier Design -- Test Setups and Results -- Conclusion -- Index.
Sommario/riassunto	This work describes the design flow for the optimization of linear CMOS power amplifiers from the first steps of the design to the final IC implementation and tests. The authors also focus on design guidelines for the inductor's geometrical characteristics for power applications and cover their measurement and characterization. Additionally, a model is proposed which facilitates design in terms of transistor sizing, required inductor quality factors or minimum supply voltage. The model considers the limitations that CMOS processes can impose on implementation. The book also details various techniques and architectures that allow for optimization.