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	Nota di contenuto	A Low TC Voltage Reference Generator Suitable for Low Temperature Applications -- A Java Card Virtual Machine Design Based on Off-card/On-card Co-design Pre-processing -- ACCDSE: A Design Space Exploration Framework for Convolutional Neural Network Accelerator -- A programmable pre-emphasis transmitter for SerDes in 40nm CMOS -- A Radiation-Immune Low-Jitter High-Frequency PLL for SerDes -- A high throughput power-efficient optical memory subsystem for kilo-core processor -- RBPCCMRelax Blocking Parallel Collective Communication Mechanism Base on Hardware with Scalability -- Experiment and Finite Element Analysis of Stochastic Vibration of Severe Environment Computer.
	Sommario/riassunto	This book constitutes the refereed proceedings of the 21st CCF Conference on Computer Engineering and Technology, NCCET 2017, held in Xiamen, China, in August 2017. The 13 full papers presented

were carefully reviewed and selected from 108 submissions. They address topics such as processor architecture; application specific processors; computer application and software optimization; technology on the horizon.