

1. Record Nr.	UNINA9910272058803321
Titolo	Sustainable development : a challenge for Europe's urban regions : 3rd Biennial of Towns and Town Planners in Europe : [Herne/Ruhrgebiet, 14.-17. September 1999]
Pubbl/distr/stampa	Berlin : SRL, 1999
Descrizione fisica	164 p. : ill. ; 30 cm
Locazione	FARBC
Collocazione	INU C 646
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
2. Record Nr.	UNINA9911020181503321
Autore	Wells Gordon
Titolo	Learning for Life in the 21st Century : Sociocultural Perspectives on the Future of Education
Pubbl/distr/stampa	Hoboken, : Wiley, 2008
ISBN	9786611322588 9781281322586 128132258X 9780470703366 0470703369 9780470753545 0470753544 9780470752081 0470752084
Descrizione fisica	1 online resource (314 p.)
Altri autori (Persone)	ClaxtonGuy
Disciplina	306.43 370.1
Soggetti	Aims and objectives Education Educational anthropology Learning Social Sciences Theory & Practice of Education

Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di contenuto	<p>Learning for Life in the 21st Century; Contents; List of Contributors; 1 Introduction: Sociocultural Perspectives on the Future of Education; Part I Issues and Developments in Sociocultural Theory; 2 Education for the Learning Age: A Sociocultural Approach to Learning to Learn; 3 Becoming the Village: Education Across Lives; 4 The Gift of Confidence: A Vygotskian View of Emotions; 5 From Activity to Directivity: The Question of Involvement in Education; 6 Sociocultural Perspectives on Assessment; 7 Teaching, Learning, and Development: A Post-Vygotskian Perspective</p> <p>Part II Pre-School and School-Age Learning and Development8 Emerging Learning Narratives: A Perspective from Early Childhood Education; 9 Semiotic Mediation and Mental Development in Pluralistic Societies: Some Implications for Tomorrow's Schooling; 10 Learning to Argue and Reason Through Discourse in Educational Settings; 11 Developing Dialogues; 12 Supporting Students' Learning of Significant Mathematical Ideas; 13 A Developmental Teaching Approach to Schooling; 14 Standards for Pedagogy: Research, Theory and Practice;</p> <p>Part III Post-Compulsory, Adult and Professional Learning</p> <p>15 Inquiry as an Orientation for Learning, Teaching and Teacher Education16 Can a School Community Learn to Master Its Own Future? An Activity- Theoretical Study of Expansive Learning Among Middle School Teachers; 17 Cultural Historical Activity Theory and the Expansion of Opportunities for Learning After School; 18 Building a Community of Educators versus Effecting Conceptual Change in Individual Students: Multicultural Education for Pre-service Teachers; 19 Organizing Excursions Into Specialist Discourse Communities: A Sociocultural Account of University Teaching; 20 Afterword;</p> <p>Bibliography Index</p>
Sommario/riassunto	<p>United by the belief that the most significant factor in shaping the minds of young people is the cultural setting in which learning takes place, the twenty eminent contributors to this volume present new thinking on education across the boundaries of school, home, work and community.</p>

3. Record Nr.	UNINA9910437590403321
Titolo	Computer Engineering and Technology : 16th National Conference, NCCET 2012, Shanghai, China, August 17-19, 2012, Revised Selected Papers // edited by Weixia Xu, Liquan Xiao, Pingjing Lu, Jinwen Li, Chengyi Zhang
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2013
ISBN	3-642-35898-5
Edizione	[1st ed. 2013.]
Descrizione fisica	1 online resource (XIV, 263 p. 164 illus.)
Collana	Communications in Computer and Information Science, , 1865-0937 ; ; 337
Disciplina	004.1
Soggetti	Microprocessors Computer architecture Computer arithmetic and logic units Computer storage devices Memory management (Computer science) Logic design Computers Processor Architectures Arithmetic and Logic Structures Computer Memory Structure Logic Design Hardware Performance and Reliability
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Session 1: Microprocessor and Implementation -- A Method of Balancing the Global Multi-mode Clock Network in Ultra-large Scale CPU -- Hardware Architecture for the Parallel Generation of Long-Period Random Numbers Using MT Method -- MGTE: A Multi-level Hybrid Verification Platform for a 16-Core Processor -- An Efficient Parallel SURF Algorithm for Multi-core Processor -- A Study of Cache Design in Stream Processor -- Design and Implementation of Dynamically Reconfigurable Token Coherence Protocol for Many-Core

Processor -- Dynamic and Online Task Scheduling Algorithm Based on Virtual Compute Group in Many-Core Architecture -- ADL and High Performance Processor Design -- Session 2: Design of Integration Circuit -- The Design of the ROHC Header Compression Accelerator -- A Hardware Implementation of Nussinov RNA Folding Algorithm -- A Configurable Architecture for 1-D Discrete Wavelet Transform -- A Comparison of Folded Architectures for the Discrete Wavelet Transform -- A High Performance DSP System with Fault Tolerant for Space Missions -- The Design and Realization of Campus Information Release Platform Based on Android Framework -- A Word-Length Optimized Hardware Gaussian Random Number Generator Based on the Box-Muller Method -- Session 3: I/O Interconnect -- DAMQ Sharing Scheme for Two Physical Channels in High Performance Router -- Design and Implementation of Dynamic Reliable Virtual Channel for Network-on-Chip -- HCCM: A Hierarchical Cross-Connected Mesh for Network on Chip -- Efficient Broadcast Scheme Based on Sub-network Partition for Many-Core CMPs on Gem5 Simulator -- A Quick Method for Mapping Cores Onto 2D-Mesh Based Networks on Chip -- Session 4: Measurement, Verification, and Others -- A Combined Hardware/Software Measurement for ARM Program Execution Time -- A Low-Complexity Parallel Two-Sided Jacobi Complex SVD Algorithm and Architecture for MIMO Beamforming Systems -- A Thermal-Aware Task Mapping Algorithm for Coarse Grain Reconfigurable Computing System -- DC Offset Mismatch Calibration for Time-Interleaved ADCs in High-Speed OFDM Receivers -- An Novel Graph Model for Loop Mapping on Coarse-Grained Reconfigurable Architectures -- Memristor Working Condition Analysis Based on SPICE Model -- On Stepsize of Fast Subspace Tracking Methods.

Sommario/riassunto

This book constitutes the refereed proceedings of the 16th National Conference on Computer Engineering and Technology, NCCET 2012, held in Shanghai, China, in August 2012. The 27 papers presented were carefully reviewed and selected from 108 submissions. They are organized in topical sections named: microprocessor and implementation; design of integration circuit; I/O interconnect; and measurement, verification, and others.
