

1. Record Nr.	UNISA996390123203316
Autore	Wilson John, M.A.
Titolo	A treatise of religion and governmentt [sic] [[electronic resource]] : with reflexions vpon the cause and cure of Englands late distempers and present dangers. The argument vvwhether Protestantcy is less dangerous to the soul, or more advantagious to the state, then the Roman Catholick religion? The conclusion that piety and policy are mistaken in promoting Protestantcy, and persecuting Popery by penal and sanguinary statuts
Pubbl/distr/stampa	[London, : s.n.], Permissu superiorum. An : Dom : M.DC.LXX. [1670]
Descrizione fisica	[36], 614, 34, [8] p
Soggetti	Church and state - Protestant churches Church and state - Great Britain Protestantism
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Attributed by Wing to Peter Talbot. Place of publication from Wing. With marginal notes. P. 32 (second sequence) is misnumbered 34. In this issue, the errata on b4 begins: The absence of the author; and the compositor's not understanding English, Reproduction of the original at the Henry E. Huntington Library and Art Gallery.
Sommario/riassunto	eebo-0113

2. Record Nr.	UNINA9910254848603321
Autore	Ben Abdallah Abderazek
Titolo	Advanced Multicore Systems-On-Chip : Architecture, On-Chip Network, Design // by Abderazek Ben Abdallah
Pubbl/distr/stampa	Singapore : , : Springer Singapore : , : Imprint : Springer, , 2017
ISBN	981-10-6092-4
Edizione	[1st ed. 2017.]
Descrizione fisica	1 online resource (XXVI, 273 p. 171 illus., 88 illus. in color.)
Disciplina	004
Soggetti	Computer hardware Electrical engineering Computer Hardware Electrical Engineering
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	1. Introduction to Multicore Systems On-Chip -- 2. Multicore SoCs Design Methods -- 3. Mulicore SoCs Organization -- 4. Multicore SoC On-Chip Interconnection Networks -- 5. Advanced Multicore SoC Interconnects -- 6. 3D Integration Technology for Multicore Systems On-Chip -- 7. Parallelizing Compiler for Single and Multicore Computing -- 8. Power Optimization Techniques for Multicore SoCs -- 9. Real Deign of Embedded Multicore SoC for Health Monitoring.
Sommario/riassunto	From basic architecture, interconnection, and parallelization to power optimization, this book provides a comprehensive description of emerging multicore systems-on-chip (MCSocCs) hardware and software design. Highlighting both fundamentals and advanced software and hardware design, it can serve as a primary textbook for advanced courses in MCSocCs design and embedded systems. The first three chapters introduce MCSocCs architectures, present design challenges and conventional design methods, and describe in detail the main building blocks of MCSocCs. Chapters 4, 5, and 6 discuss fundamental and advanced on-chip interconnection network technologies for multi and many core SoCs, enabling readers to understand the microarchitectures for on-chip routers and network interfaces that are essential in the context of latency, area, and power constraints. With

the rise of multicore and many-core systems, concurrency is becoming a major issue in the daily life of a programmer. Thus, compiler and software development tools are critical in helping programmers create high-performance software. Programmers should make sure that their parallelized program codes will not cause race condition, memory-access deadlocks, or other faults that may crash their entire systems. As such, Chapter 7 describes a novel parallelizing compiler design for high-performance computing. Chapter 8 provides a detailed investigation of power reduction techniques for MCMs at component and network levels. It discusses energy conservation in general hardware design, and also in embedded multicore system components, such as CPUs, disks, displays and memories. Lastly, Chapter 9 presents a real embedded MCMs system design targeted for health monitoring in the elderly. .
