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| 1. Record Nr. | UNINA990005407340403321 |
| Autore | Hogarth, David George <1862-1927> |
| Titolo | Accidents of an antiquary's Life / by D.G. Hogarth |
| Pubbl/distr/stampa | London, : MacMillan & Co., 1910 |
| Descrizione fisica | X, 176 p., 40 tav. ; 23 cm |
| Locazione | FLFBC |
| Collocazione | ARCH. O 080 8 |
| Lingua di pubblicazione | Italiano |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |
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| 2. Record Nr. | UNINA9910254350803321 |
| Autore | Banerjee Amal |
| Titolo | Automated Electronic Filter Design / / by Amal Banerjee |
| Pubbl/distr/stampa | Cham : , : Springer International Publishing : , : Imprint : Springer, , 2017 |
| ISBN | 3-319-43470-5 |
| Edizione | [1st ed. 2017.] |
| Descrizione fisica | 1 online resource (VIII, 87 p. 35 illus., 13 illus. in color.) |
| Disciplina | 621.3815 |
| Soggetti | Electronic circuits
Electronics
Microelectronics
Circuits and Systems
Electronic Circuits and Devices
Electronics and Microelectronics, Instrumentation |
| Lingua di pubblicazione | Inglese |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |
| Nota di bibliografia | Includes bibliographical references at the end of each chapters. |
| Nota di contenuto | Introduction and Problem Statement -- Automated Electronic Filter |

Design Scheme -- Automated Electronic Filter Design
Algorithm/Scheme Implementation and Design Examples -- Higher
Frequencies (100's of MHz – 10's of GHz) - Physical Constraints and
Distributed Filters -- Summary and Conclusion -- Appendix A Using
the Automated Filter Design Tool.

Sommario/riassunto

This book describes a novel, efficient and powerful scheme for designing and evaluating the performance characteristics of any electronic filter designed with predefined specifications. The author explains techniques that enable readers to eliminate complicated manual, and thus error-prone and time-consuming, steps of traditional design techniques. The presentation includes demonstration of efficient automation, using an ANSI C language program, which accepts any filter design specification (e.g. Chebyshev low-pass filter, cut-off frequency, pass-band ripple etc.) as input and generates as output a SPICE(Simulation Program with Integrated Circuit Emphasis) format netlist. Readers then can use this netlist to run simulations with any version of the popular SPICE simulator, increasing accuracy of the final results, without violating any of the key principles of the traditional design scheme.
