

1. Record Nr.	UNISA990005802040203316
Autore	MARCHI, Clelia
Titolo	Il tuo nome sulla neve : gnanca na busia : il romanzo di una vita scritta su un lenzuolo / Clelia Marchi ; prefazione di Carmen Covito ; prefazione alla prima edizione di Saverio Tutino
Pubbl/distr/stampa	Milano : Il Saggiatore, 2012
ISBN	978-88-428-1868-7 978-88-428-2054-3
Descrizione fisica	93 p. : ill. ; 20 cm
Collana	La Cultura ; 792
Disciplina	858.91402
Soggetti	Marchi, Clelia Diari
Collocazione	VI.3.A. 404 VI.3.A. 404 a
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

2. Record Nr.	UNINA9910460481003321
Autore	Myers Raymond H.
Titolo	Response surface methodology : process and product optimization using designed experiments // Raymond H. Myers, Douglas C. Montgomery, Christine M. Anderson-Cook
Pubbl/distr/stampa	Hoboken, New Jersey : , : Wiley, , 2016 ©2016
ISBN	1-118-91602-6
Edizione	[Fourth edition.]
Descrizione fisica	1 online resource (855 p.)
Collana	Wiley Series in Probability and Statistics
Disciplina	519.5/7
Soggetti	Experimental design Response surfaces (Statistics) Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.

3. Record Nr.	UNINA9910254227303321
Autore	Tlelo-Cuautle Esteban
Titolo	Engineering Applications of FPGAs : Chaotic Systems, Artificial Neural Networks, Random Number Generators, and Secure Communication Systems // by Esteban Tlelo-Cuautle, José de Jesús Rangel-Magdaleno, Luis Gerardo de la Fraga
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2016
ISBN	3-319-34115-4
Edizione	[1st ed. 2016.]
Descrizione fisica	1 online resource (XVI, 222 p. 204 illus., 130 illus. in color.)
Disciplina	621.3815
Soggetti	Electronic circuits Microprocessors Computer architecture Electronics Electronic Circuits and Systems Processor Architectures Electronics and Microelectronics, Instrumentation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction to field programmable gate arrays -- VHDL -- Matlab-Simulink Co-simulation -- Chaos generators -- Artificial neural networks for time series prediction -- Random number generators -- Secure communication system -- Challenges in engineering applications.
Sommario/riassunto	This book offers readers a clear guide to implementing engineering applications with FPGAs, from the mathematical description to the hardware synthesis, including discussion of VHDL programming and co-simulation issues. Coverage includes FPGA realizations such as: chaos generators that are described from their mathematical models; artificial neural networks (ANNs) to predict chaotic time series, for which a discussion of different ANN topologies is included, with different learning techniques and activation functions; random number generators (RNGs) that are realized using different chaos generators,

and discussions of their maximum Lyapunov exponent values and entropies. Finally, optimized chaotic oscillators are synchronized and realized to implement a secure communication system that processes black and white and grey-scale images. In each application, readers will find VHDL programming guidelines and computer arithmetic issues, alongwith co-simulation examples with Active-HDL and Simulink. Readers will benefit from this practical guide to implementing a variety of engineering applications from VHDL programming and co-simulation issues, to FPGA realizations of chaos generators, ANNs for chaotic time-series prediction, RNGs and chaotic secure communications for image transmission.
