1. Record Nr. UNINA9910697167103321

Autore	Rogers James L
Titolo	Development of a genetic algorithm to automate clustering of a dependency structure matrix [[electronic resource] /] / by James L. Rogers, John J. Korte and Vincent J. Bilardo, Jr
Pubbl/distr/stampa	Hampton, Va. : , : National Aeronautics and Space Administration, Langley Research Center, , [2006]
Descrizione fisica	1 online resource (14 pages) : color illustrations
Collana	NASA/TM ; ; 2006-214279
Altri autori (Persone)	KorteJohn J BilardoVincent J., Jr.
Soggetti	Genetic algorithms Matrices (mathematics) Computer programs Optimization Automation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Title from title screen (viewed on Oct. 11, 2011). "February 2006."
Nota di bibliografia	Includes bibliographical references (pages 13-14).

2. Record Nr. UNINA9910163119703321

Titolo	Normally-Off Computing / / edited by Takashi Nakada, Hiroshi Nakamura
Pubbl/distr/stampa	Tokyo : , : Springer Japan : , : Imprint : Springer, , 2017
Edizione	[1st ed. 2017.]
Descrizione fisica	1 online resource (VII, 136 p. 106 illus., 74 illus. in color.)
Disciplina	004.53
Soggetti	Computer storage devices Electrical engineering Renewable energy resources Memory Structures Electrical Engineering Renewable and Green Energy
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references.
Nota di contenuto	1. Introduction -- 2. Low-power circuit technologies -- 3. Non-volatile memories -- 4. Normally-Off Computing -- 5. Technologies for realizing Normally-Off Computing -- 6. Research and Development of Normally-Off Computing – NEDO Project – -- 7. Related Research & Development -- 8. Conclusion.
Sommario/riassunto	As a step toward ultimate low-power computing, this book introduces normally-off computing, which involves inactive components of computer systems being aggressively powered off with the help of new non-volatile memories (NVMs). Because the energy consumption of modern information devices strongly depends on both hardware and software, co-design and co-optimization of hardware and software are indispensable to improve energy efficiency. The book discusses various topics including (1) details of low-power technologies including power gating, (2) characteristics of several new-generation NVMs, (3) normally-off computing architecture, (4) important technologies for implementing normally-off computing, (5) three practical implementations: healthcare, mobile information devices, and sensor network systems for smart city applications, and (6) related research

and development. Bridging computing methodology and emerging memory devices, the book is designed for both hardware and software designers, engineers, and developers as comprehensive material for understanding normally-off computing.