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Sommario/riassunto	<p>A technical basis for implementation of electronic power subsystems is provided in this recommended practice. It is intended for electronic systems engineers and integrators, electronic power subsystem designers and integrators, as well as power element manufacturers and suppliers. It addresses system-level issues in element or subsystem integration, adaptation, and accommodation. It also defines system-level interface parameters, test methods, and test conditions. It provides for a systems engineering approach to acquisition, adaptation, and integration of electronic power subsystems, facilitates and promotes a modular approach to element or subsystem integration, and enables effective communication between the end users of power electronics and their manufacturers or suppliers. Scope: The Recommended Practice applies to ac-dc and dc-dc electronic power subsystems. The range of power subsystems includes dc, single phase, and three-phase inputs, with elements having power levels from a fraction of a watt to 20 kW. The voltage range is 600 V and below, at a frequency or frequencies of dc -1 kHz. The recommended practice may be used outside the range where applicable. Purpose: There are no defined interfaces for power electronic subsystems. The Recommended Practice is intended for designers, integrators and manufacturers of</p>

power electronics. This document provides interface definitions and application guidance, including parametric values for power electronic subsystems consisting of single or multiple elements.
