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| Nota di contenuto | Seventeenth IEEE International Workshop on Rapid System Prototyping - Cover -- Seventeenth IEEE International Workshop on Rapid System Prototyping - Title -- Seventeenth IEEE International Workshop on Rapid System Prototyping -Copyright -- Seventeenth IEEE International Workshop on Rapid System Prototyping - TOC -- Message from the General Chairs -- Message from the Organizing Chair -- Message from the Program Chairs -- Acknowledgments -- Conference Committees -- Dynamic Mapping of Runtime Information Models for Debugging Embedded Software -- Principles for System Prototype and Verification Using Metamodel Based Transformations -- Creation and Validation of Embedded Assertion Statecharts -- Early Embedded Software Design Space Exploration Using UML-Based Estimation. |
| Sommario/riassunto | RSP Workshop discusses the fast development of systems and focuses on techniques that will produce high-quality software and hardware systems through original combinations of modeling, design techniques, testing methods, and validation principles. RSP 2006 covers technologies as diverse as software systems, reconfigurable computing, and custom hardware applied to applications ranging from aerospace to telecommunications systems and from military to commercial systems. The paper topics reflect the continued evolution of the field. The program includes recent advances in software prototyping, |

hardware prototyping, hardware/software co-design, design methodologies, and advanced simulation. RSP also looks to accelerate the advancement of the theory and practice of rapid system prototyping both in traditional and new areas of computer system development.
