

1. Record Nr.	UNINA9910789920203321
Titolo	The History of Western philosophy of religion . Volume 4 Nineteenth-century philosophy of religion // edited by Graham Oppy and Nick Trakakis [[electronic resource]]
Pubbl/distr/stampa	Durham : , : Acumen Publishing, , 2009
ISBN	1-315-72960-1 1-306-45638-X 1-317-54641-5 1-317-54642-3 1-84465-466-4
Descrizione fisica	1 online resource (xv, 333 pages) : digital, PDF file(s)
Disciplina	210.9
Soggetti	Religion - Philosophy
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Title from publisher's bibliographic system (viewed on 02 Oct 2015).
Nota di bibliografia	Includes bibliographical references and indexes.
Nota di contenuto	Cover; Half Title; Title Page; Copyright Page; Table of Contents; Editorial Introduction; Contributors; 1. Nineteenth-Century Philosophy of Religion: An Introduction; 2. Johann Gottlieb Fichte; 3. Friedrich Schleiermacher; 4. G. W. F. Hegel; 5. Friedrich Schelling; 6. Arthur Schopenhauer; 7. Auguste Comte; 8. John Henry Newman; 9. Ralph Waldo Emerson; 10. Ludwig Feuerbach; 11. John Stuart Mill; 12. Charles Darwin; 13. Søren Kierkegaard; 14. Karl Marx and Friedrich Engels; 15. Wilhelm Dilthey; 16. Edward Caird; 17. Charles S. Peirce; 18. Friedrich Nietzsche; 19. Josiah Royce; 20. Sigmund Freud 21. Emile DurkheimChronology; Bibliography; Index
Sommario/riassunto	An international team of over 100 leading scholars has been brought together to provide authoritative exposition of how history's most important philosophical thinkers from antiquity to the present day have sought to analyse the concepts and tenets central to Western religious belief, especially Christianity. Volume 4 comprises an introduction, presenting an overview of Nineteenth-Century philosophy of religion, and a series of essays on Nineteenth-Century philosophers, including Johann Gottlieb Fichte, G. W. F. Hegel, Arthur Schopenhauer,

Karl Marx and Friedrich Engels, Friedrich Nietzsche, Josiah Royce, John Henry Newman and Emile Durkheim. Each essay opens with a brief biography, outlines and analyses the philosophers contribution to thinking on religion, and concludes with key further reading. The volume closes with a chronology, presenting a contextual guide to the main religious, political, cultural and artistic events of the period. This volume is an excellent tool for those undertaking research in this area.

2. Record Nr.	UNINA9910144916003321
Titolo	Field Programmable Logic and Applications [[electronic resource]] : 7th International Workshop, FPL '97, London, UK, September, 1-3, 1997, Proceedings. // edited by Wayne Luk, Peter Y.K. Cheung, Manfred Glesner
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 1997
ISBN	3-540-69557-5
Edizione	[1st ed. 1997.]
Descrizione fisica	1 online resource (XII, 512 p.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 1304
Disciplina	621.39/5
Soggetti	Computer engineering Computer architecture Programming languages (Electronic computers) Computer hardware Computational complexity Logic, Symbolic and mathematical Computer Engineering Computer System Implementation Programming Languages, Compilers, Interpreters Computer Hardware Complexity Mathematical Logic and Formal Languages
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph

Multicontext dynamic reconfiguration and real-time probing on a novel mixed signal programmable device with on-chip microprocessor -- CAD-oriented FPGA and dedicated CAD system for telecommunications -- Rothko: A three dimensional FPGA architecture, its fabrication, and design tools -- Extending dynamic circuit switching to meet the challenges of new FPGA architectures -- Performance evaluation of a full speed PCI initiator and target subsystem using FPGAs -- Implementation of pipelined multipliers on Xilinx FPGAs -- The XC6200DS development system -- Thermal monitoring on FPGAs using ring-oscillators -- A reconfigurable approach to low cost media processing -- Riley-2: A flexible platform for codesign and dynamic reconfigurable computing research -- Stream synthesis for a wormhole run-time reconfigurable platform -- Pipeline morphing and virtual pipelines -- Parallel graph colouring using FPGAs -- Run-time compaction of FPGA designs -- Partial reconfiguration of FPGA mapped designs with applications to fault tolerance and yield enhancement -- A case study of partially evaluated hardware circuits: Key-specific DES -- Run-time parameterised circuits for the Xilinx XC6200 -- Automatic identification of swappable logic units in XC6200 circuitry -- Towards an expert system for a priori estimation of reconfiguration latency in dynamically reconfigurable logic -- Exploiting reconfigurability through domain-specific systems -- Technology mapping by binate covering -- VPR: a new packing, placement and routing tool for FPGA research -- Technology mapping of heterogeneous LUT-based FPGAs -- Technology-driven FSM partitioning for synthesis of large sequential circuits targeting lookup-table based FPGAs -- Technology mapping of LUT based FPGAs for delay optimisation -- Automatic Mapping of Algorithms onto multiple FPGA-SRAM Modules -- FPLD HDL synthesis employing high-level evolutionary algorithm optimisation -- An hardware/software partitioning algorithm for custom computing machines -- The Java Environment for Reconfigurable Computing -- Data scheduling to increase performance of parallel accelerators -- An operating system for custom computing machines based on the Xputer paradigm -- Fast parallel implementation of DFT using configurable devices -- Enhancing fixed point DSP processor performance by adding CPLD's as coprocessing elements -- A case study of algorithm implementation in reconfigurable hardware and software -- A reconfigurable data-localised array for morphological algorithms -- Virtual radix array processors (V-RaAP) -- An FPGA implementation of a matched filter detector for spread spectrum communications systems -- An NTSC and PAL closed caption processor -- A 800Mpixel/sec reconfigurable image correlator on XC6216 -- A reconfigurable coprocessor for a PCI-based real time computer vision system -- Real-time stereopsis using FPGAs -- FPGAs Implementation of a digital IQ demodulator using VHDL -- Hardware compilation, configurable platforms and ASICs for self-validating sensors -- PostScript™ rendering with virtual hardware -- P4: A platform for FPGA implementation of protocol boosters -- Satisfiability on reconfigurable hardware -- Auto-configurable array for GCD computation -- Structural versus algorithmic approaches for efficient adders on xilinx 5200 FPGA -- FPGA implementation of real-time digital controllers using on-line arithmetic -- A prototyping environment for fuzzy controllers -- A reconfigurable sensor-data processing system for personal robots.

This book constitutes the refereed proceedings of the 7th International Workshop on Field Programmable Logic and Applications, FPL '97, held in London, UK, in September 1997. The 51 revised full papers in the volume were carefully selected from a large number of high-quality

papers. The book is divided into sections on devices and architectures, devices and systems, reconfiguration, design tools, custom computing and codesign, signal processing, image and video processing, sensors and graphics, color and robotics, and applications.

3. Record Nr.	UNIORUON00215952
Titolo	Canada ieri e oggi : atti del 6° Convegno Internazionale di Studi Canadesi : Selva di Fasano, 27-31 marzo 1985 / [organizzato da] Associazione italiana di studi canadesi
Pubbl/distr/stampa	Fasano, : Schena, 1986
ISBN	88-7514-133-9
Descrizione fisica	v. ; 21 cm
Soggetti	Canada - Cultura - Congressi Letteratura americana - Autori canadesi - Congressi
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	V.1 : Sezione Francofona / a cura di Giovanni Dotoli e Sergio Zoppi.