1. Record Nr. UNINA9910144912003321

Titolo	Job Scheduling Strategies for Parallel Processing [[electronic resource]] : IPPS '97 Workshop, Geneva, Switzerland, April 5, 1997, Proceedings / / edited by Dror G. Feitelson, Larry Rudolph
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 1997
ISBN	3-540-69599-0
Edizione	[1st ed. 1997.]
Descrizione fisica	1 online resource (VII, 305 p.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 1291
Disciplina	005.4/3475
Soggetti	Computer architecture Operating systems (Computers) Computer programming Algorithms Microprocessors Logic design Computer System Implementation Operating Systems Programming Techniques Algorithm Analysis and Problem Complexity Processor Architectures Logic Design
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di contenuto	Theory and practice in parallel job scheduling -- Using queue time predictions for processor allocation -- A historical application profiler for use by parallel schedulers -- Memory usage in the LANL CM-5 workload -- Modeling of workload in MPPs -- PScheD Political scheduling on the CRAY T3E -- An experimental evaluation of processor pool-based scheduling for shared-memory NUMA multiprocessors -- Implementing multiprocessor scheduling disciplines -- Objective-oriented algorithm for job scheduling in parallel heterogeneous systems -- Implications of I/O for gang scheduled

	workloads -- Improved utilization and responsiveness with gang scheduling -- Global state detection using network preemption -- Performance evaluation of gang scheduling for parallel and distributed multiprogramming.
Sommario/riassunto	This book constitutes the strictly refereed post-workshop proceedings of the 1997 IPPS Workshop on Job Scheduling Strategies for Parallel Processing held in Geneva, Switzerland, in April 1997, as a satelite meeting of the IEEE/CS International Parallel Processing Symposium. The 12 revised full papers presented were carefully reviewed and revised for inclusion in the book. Also included is a detailed introduction surveying the state of the art in the area. Among the topics covered are processor allocation, parallel scheduling, massively parallel processing, shared-memory architectures, gang scheduling, etc.